

FIG. 1

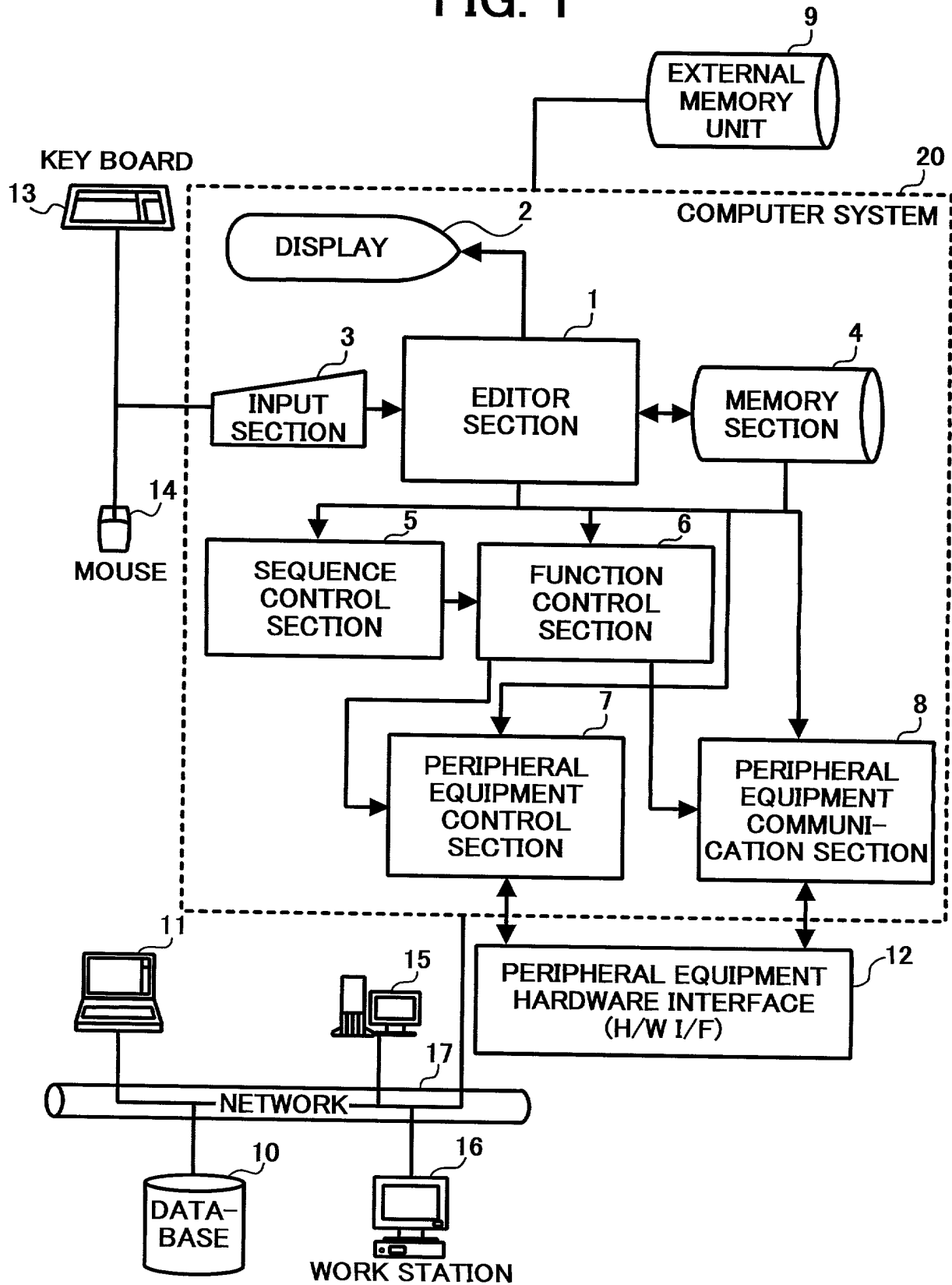
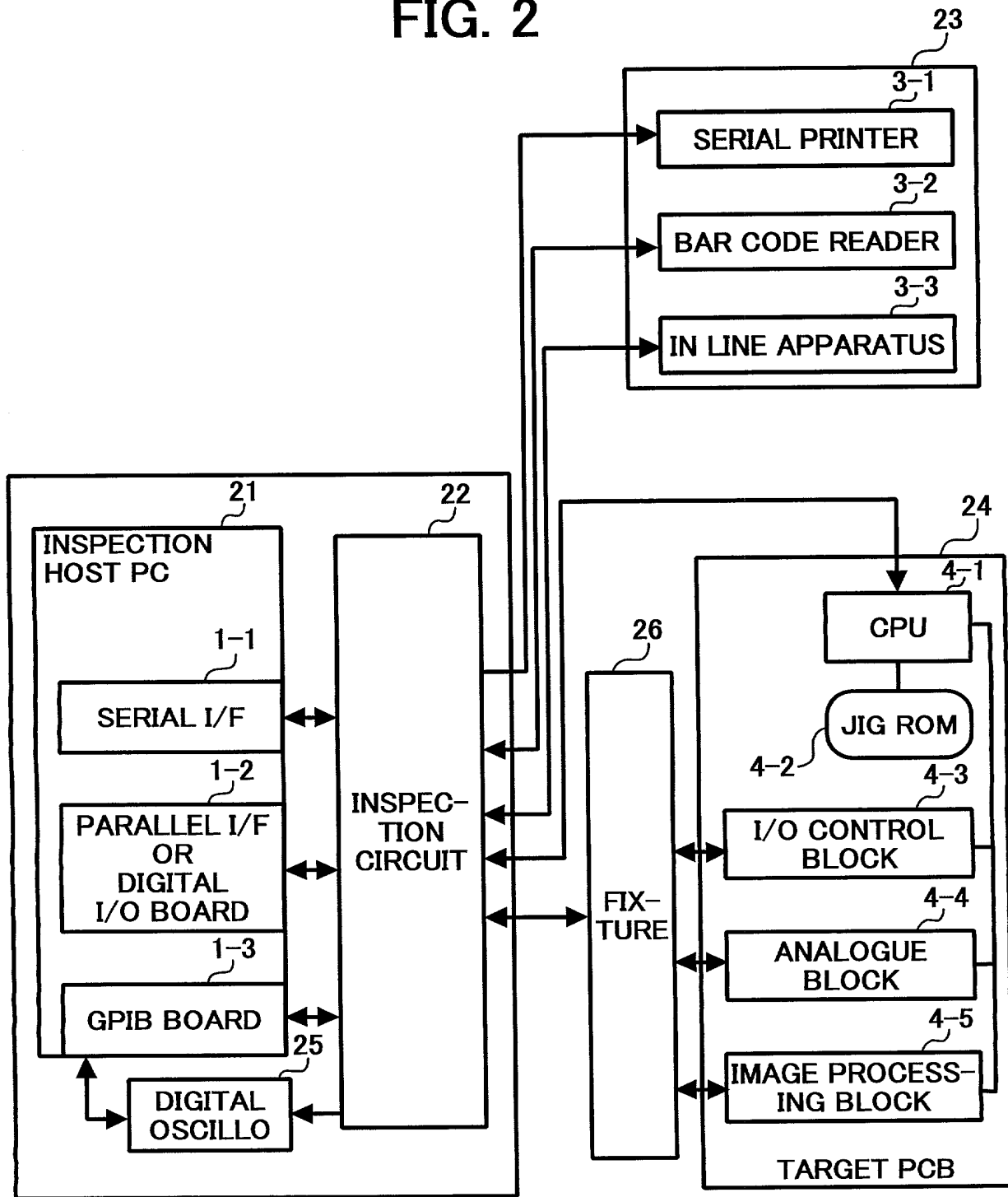


FIG. 2



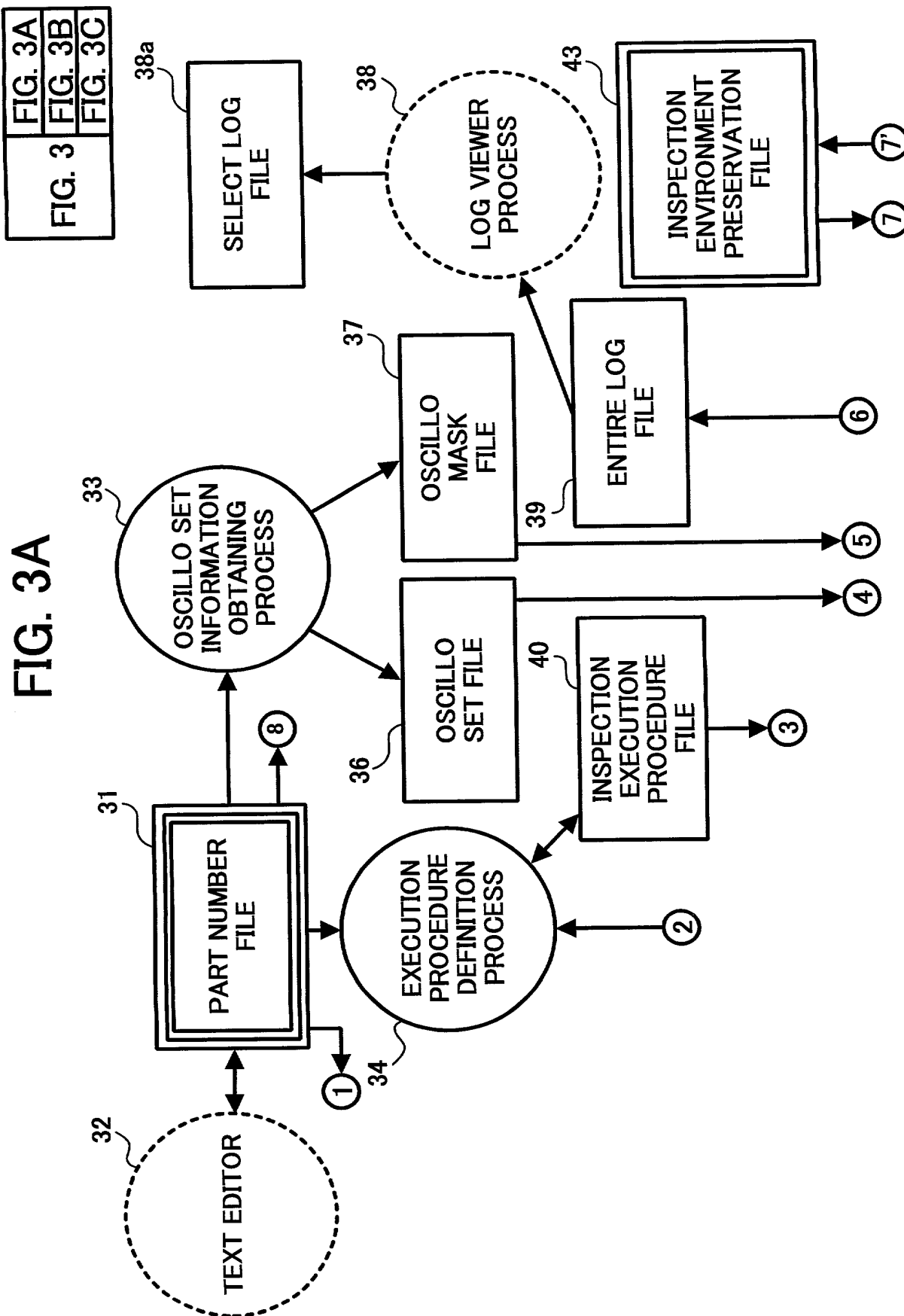
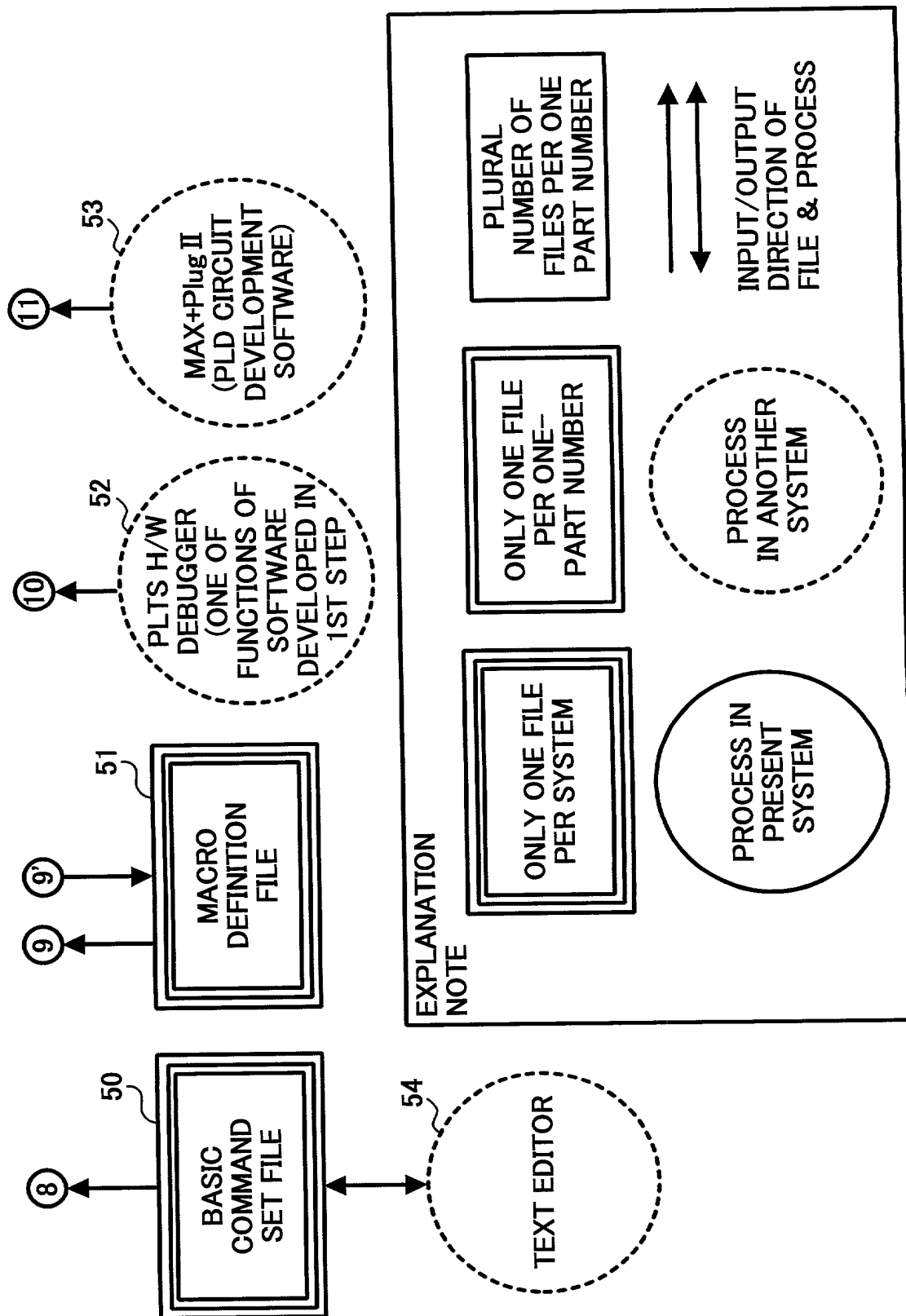


FIG. 3C



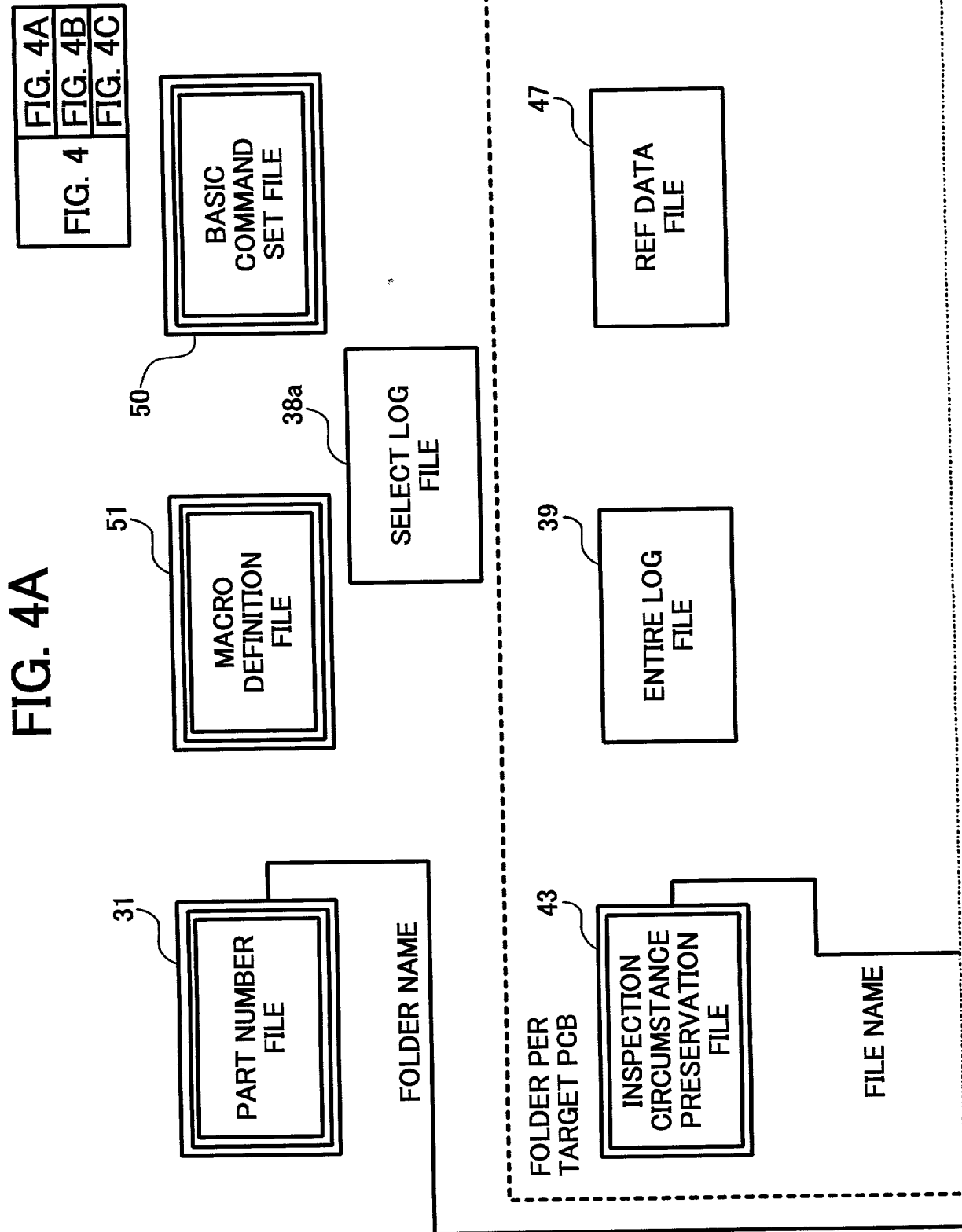


FIG. 4B

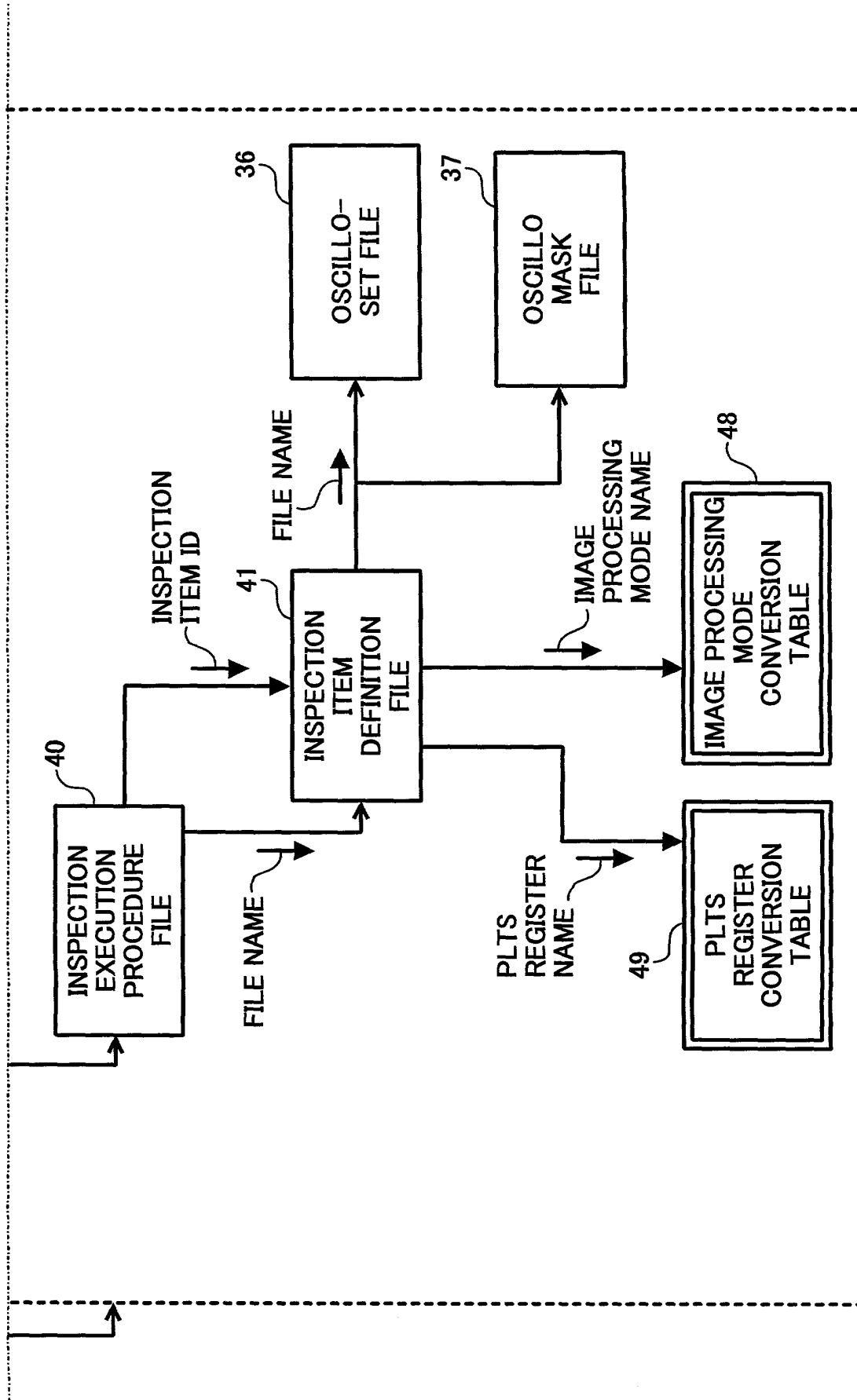


FIG. 4C

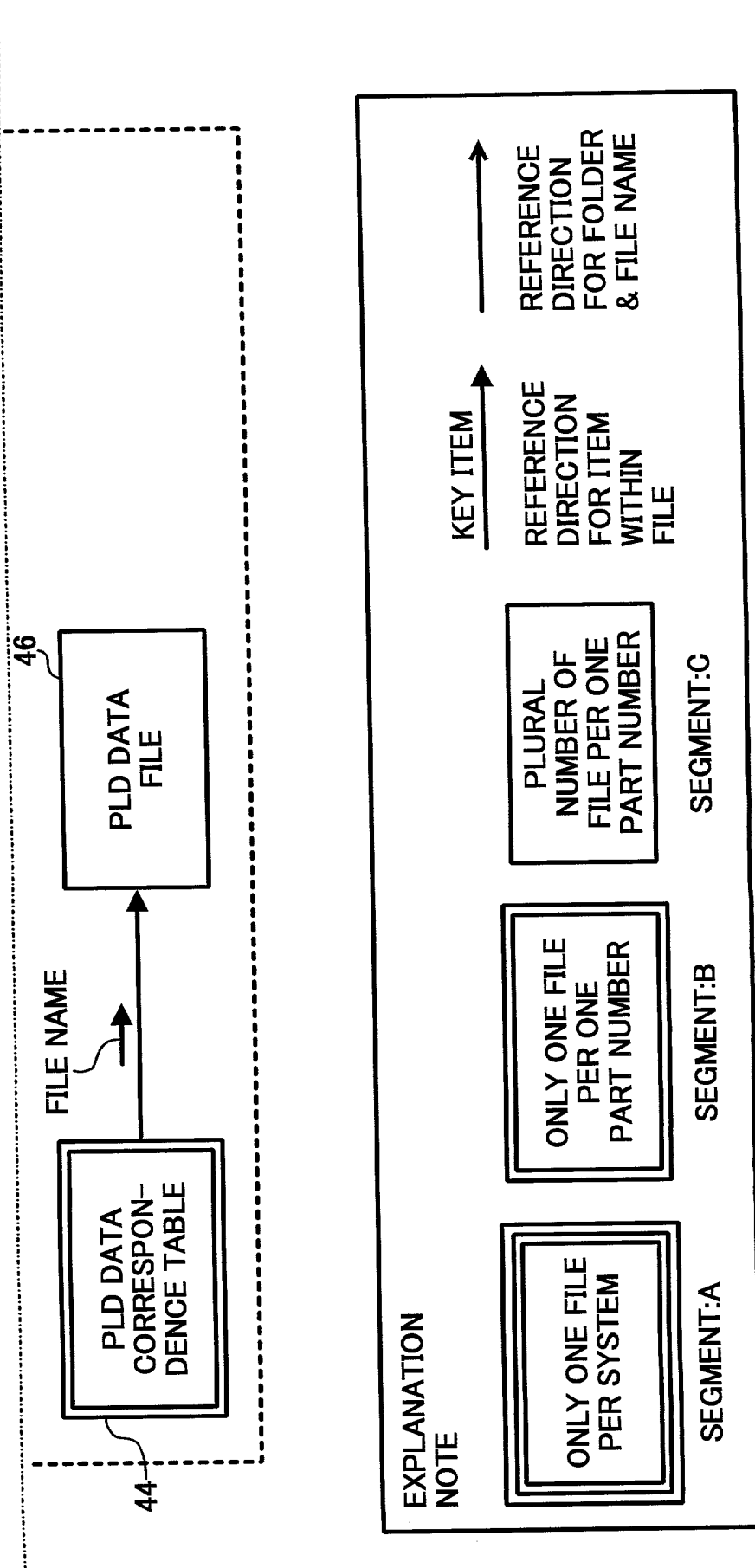


FIG. 5

TEST01		MACR001		REGWR	
t01-01	CMD_REGWR	P01_01	P01_02	P01_03	P01_04
t01-02	CMD_REGWR	P01_01	P01_05	P01_06	P01_07
t01-02_1	REG_CLR	P01_01	P01_08		
t01-03	CMD_REGWR	P01_01	P01_09	P01_10	P01_11
t01-04	CMD_WAIT	P01_12			
t01-05	CMD_REGWR	P01_01	P01_13	P01_14	P01_15
t01-06	CHK_CONECT	P01_16	P01_17	P01_18	P01_19
t01-07	CHK_DATA	P01_21	P01_22	P01_24	P01_24
t01-08	CMD_REGWR	P01_01	P01_26	P01_29	P01_29

EXAMPLE: EDITOR SECTION GUI ①

FIG. 6

EXAMPLE: EDITOR SECTION GUI 2

Test Item	Test Item
test01 PCB SET, TARGET READY	1 test01 PCB SET, TAR
test02 POWER +SV	2 test02 POWER +5V
test03 CPU OUTPUT PO	3 test03 POWER +5V
test04 CLOCK:CPUCLK	

EXAMPLE: EDITOR SECTION GUI 2

FIG. 7

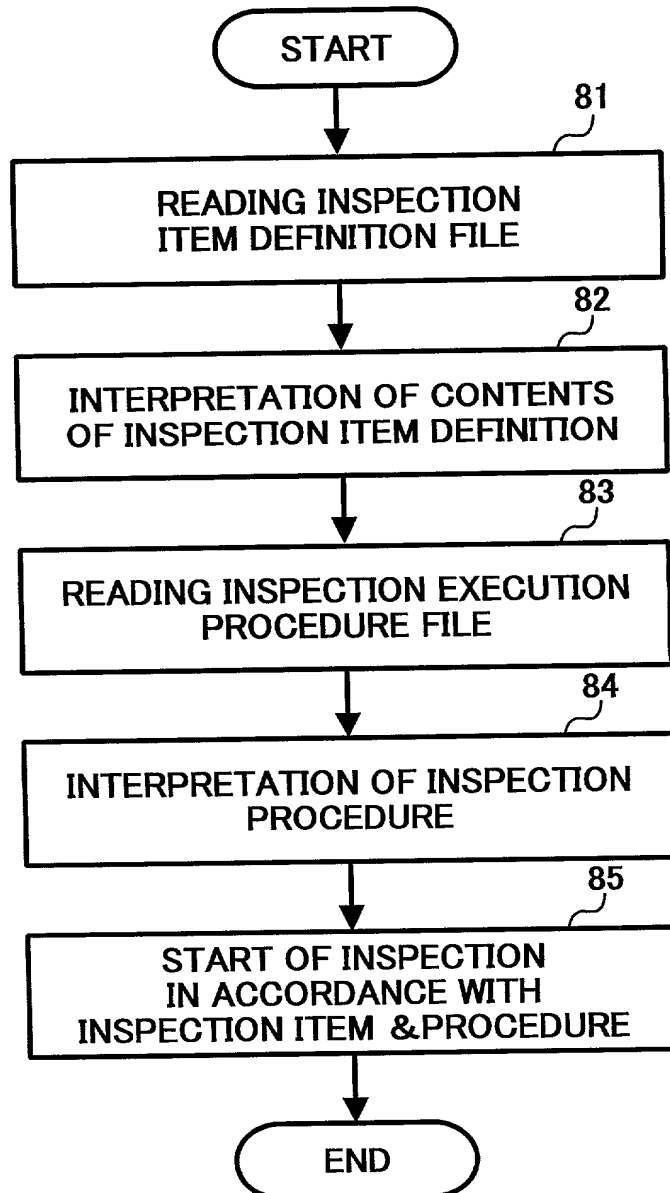
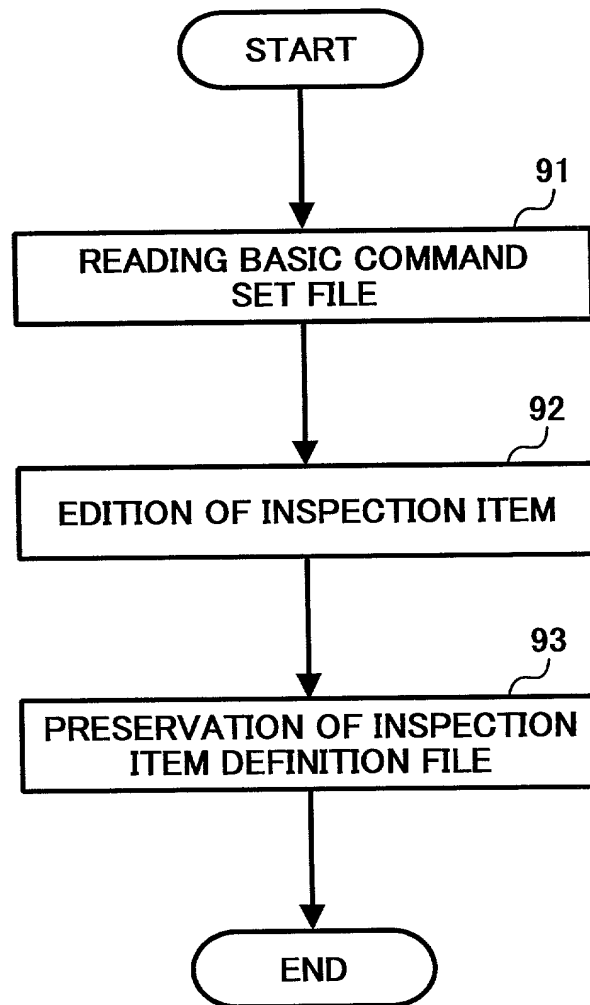


FIG. 8



FOOTER: 66354001

FIG. 9

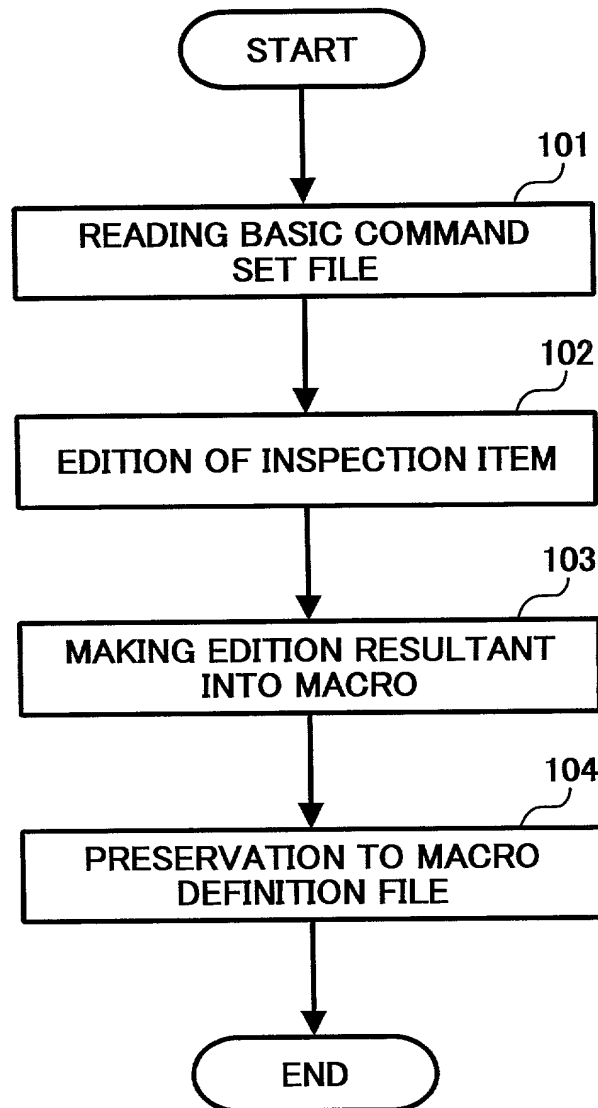


FIG. 10

Select PCB

12345678	SEAHORSE IPU	D:\APIS\SEAHORSE
87654321	ORCA SIPU	D:\APIS\ORCA

EXAMPLE: PART NUMBER SELECTION GUI

FIG. 11A

FIG. 11	FIG. 11A
	FIG. 11B
	FIG. 11C

ITEM NAME	MODEL	BYTE NUMBER	REMARKS
HEADER SECTION			
FILE ID	BYTE[]	23	"BASIC COMMAND SET@HANKS"
SEGMENTATION CHARACTER	BYTE	1	0x02C(',')
VERSION	BYTE[]	8	"V01.00"
SEGMENTATION CHARACTER	BYTE[]	2	0x0D,0x0A
COMMAND NUMBER	BYTE[]	3	001~999
SEGMENTATION	BYTE[]	2	0x0D,0x0A
SEGMENTATION	BYTE[]	2	0x0D,0x0A
INTERMEDIATE TOTAL		41	
DATA SECTION			
BASIC COMMAND			
INTERMEDIATE TOTAL			
TOTAL			

EXAMPLE: BASIC COMMAND FILE FORMAT

FIG. 11B

BASIC COMMAND FORMAT

COMMAND	PARAMETER 1	PARAMETER 2
COMMAND (A)	P1(B)	P2(B)
		...

COMMANDS & PARAMETERS ARE
COMPARTMENTALIZED BY [,:] WHEN
SEMI-COLON IS PUT AT BEGINNING, IT IS
TREATED AS COMMENTS.

A: PARAMETER NUMBER INCLUDED IN COMMAND

B: PARAMETER INFORMATION FORMAT(b1:b2:b3:....)

b:1 PARAMETER TYPE

1: NUMERICAL VALUE

2: LETTER STRING

3: WRITE REGISTER NAME 6: FILE NAME

4: READ REGISTER NAME

7: INTERNAL TABLE REFERENCE

5: PROCESSING MODE NAME

8: OTHER SELECTION

9: ERROR MESSAGE

b:2 MINIMUM VALUE

* LETTER STRING NUMBER, WHEN b1=b2

* NOTHING EXISTS SUBSEQUENT TO 3

* FOLLOWING NUMERICAL VALUE ARE ENTERED WHEN b1=7

1: CH1 2: CH2 3: EXT 4: SERIAL SELECTION

* ONE OF OPTIONS WHEN b1=8

* COLON(:) IS ENTERED BETWEEN b1 & b2 WHEN THIS PARAMETER DOES NOT EXIST

b:3 MAXIMUM VALUE

* NOTHING EXISTS AFTER b1=2

* 8 IS ONE OF OPTIONS

EXAMPLE: BASIC COMMAND FILE FORMAT

FIG. 11C

USAGE EXAMPLE

COMMAND RESPONDING TEST ID TRANSMISSION TO TARGET
T_TESTID(3),testid(2:3),rwait(1:1:99999)

WRITE COMMAND TO PLTS
P_REGWR(3),wname(3:),wrtdt(1:0:255)

EXAMPLE: BASIC COMMAND FILE FORMAT

FIG. 12

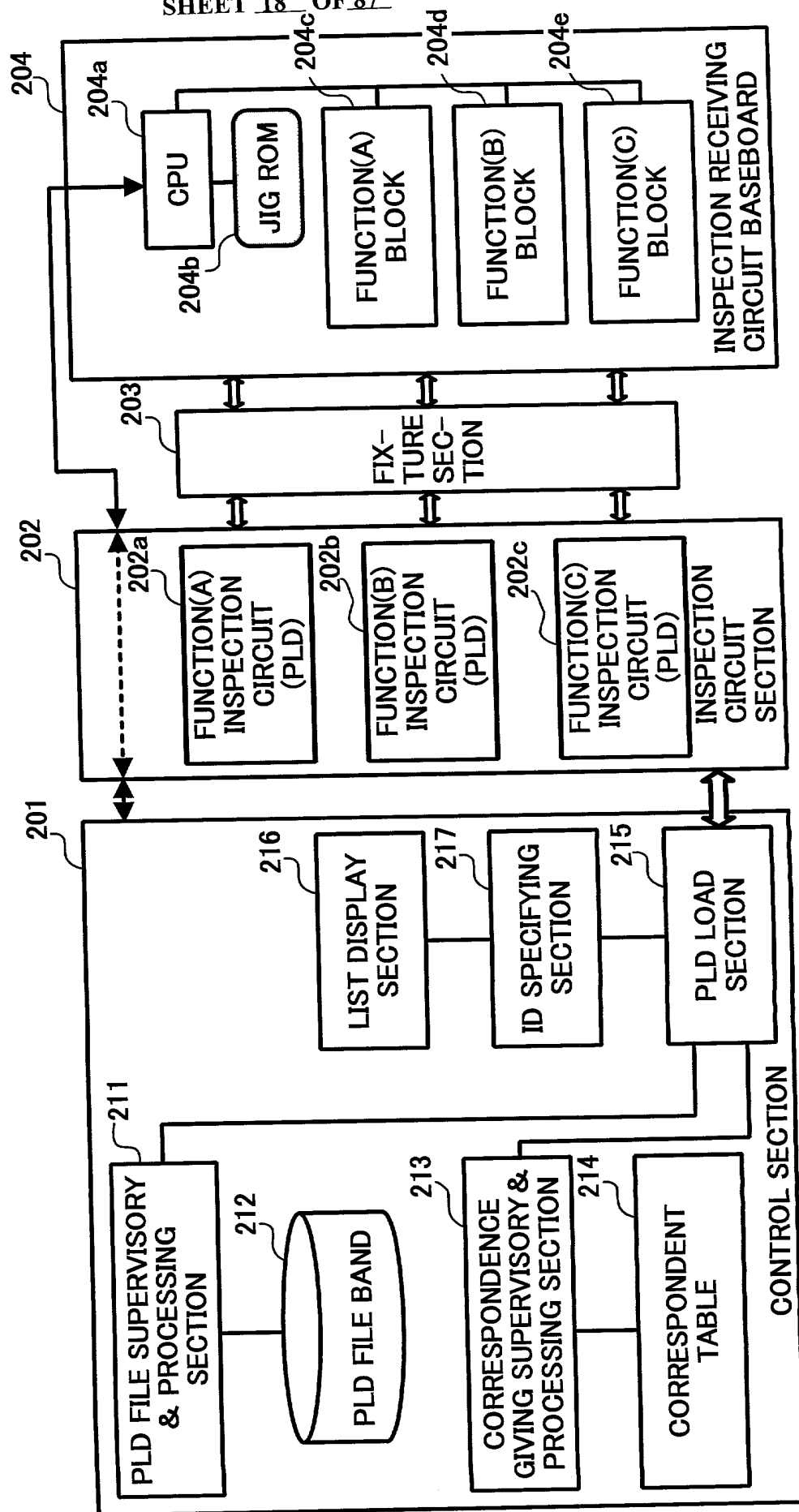


FIG. 13

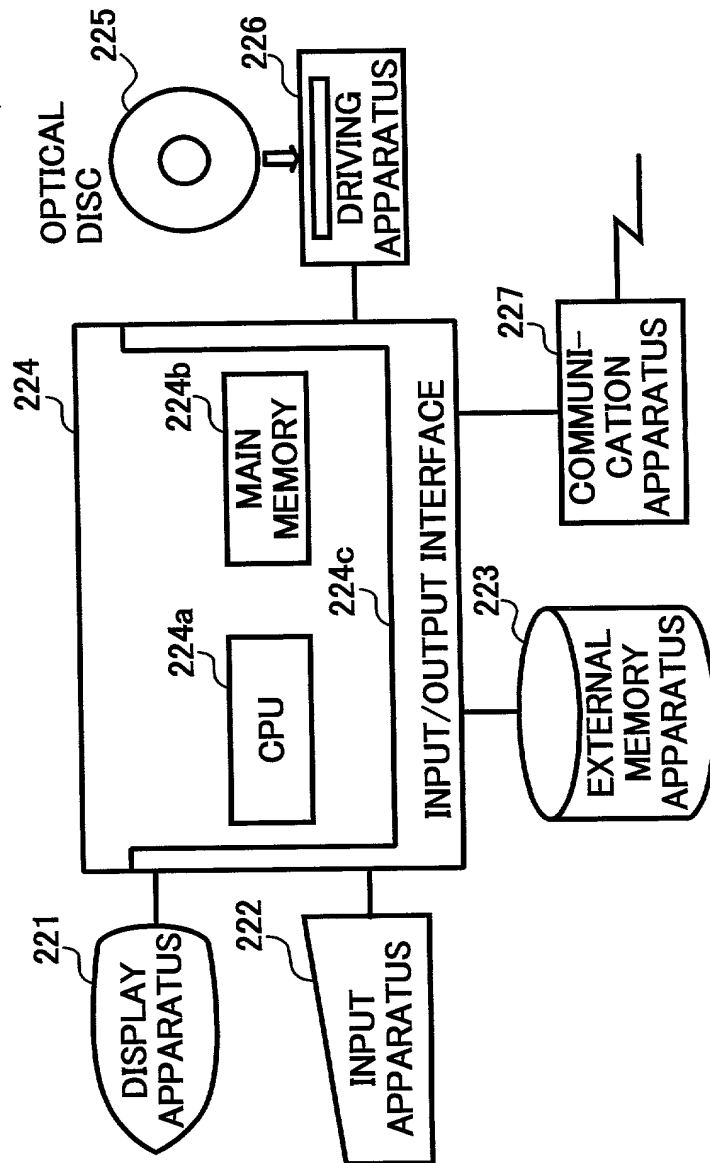


FIG. 14

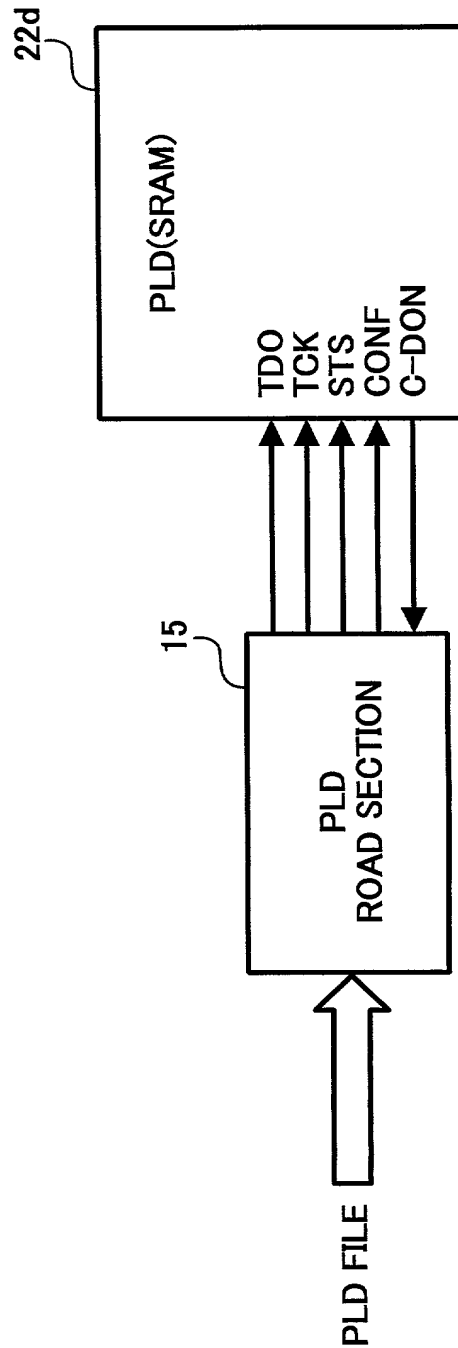
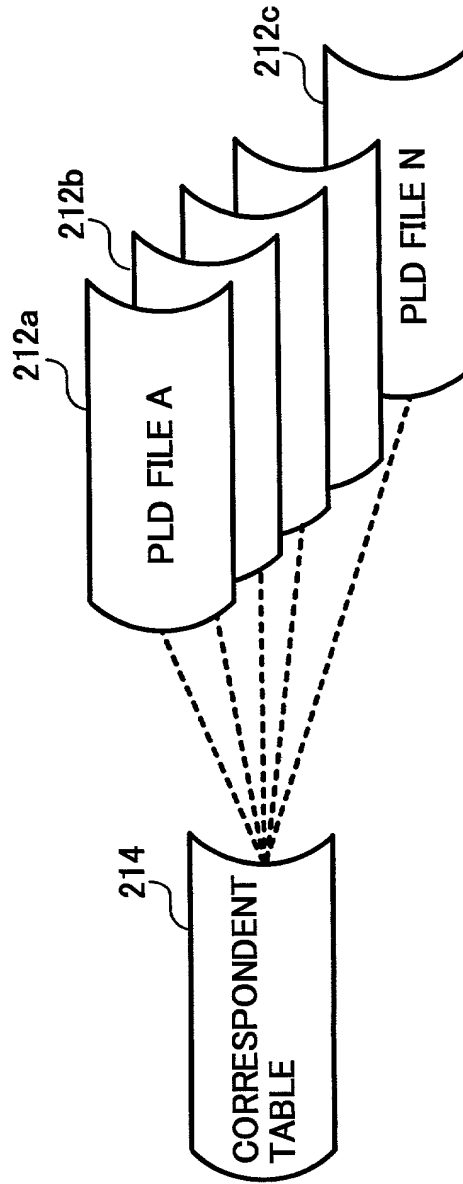


FIG. 15



16a

FIG. 17

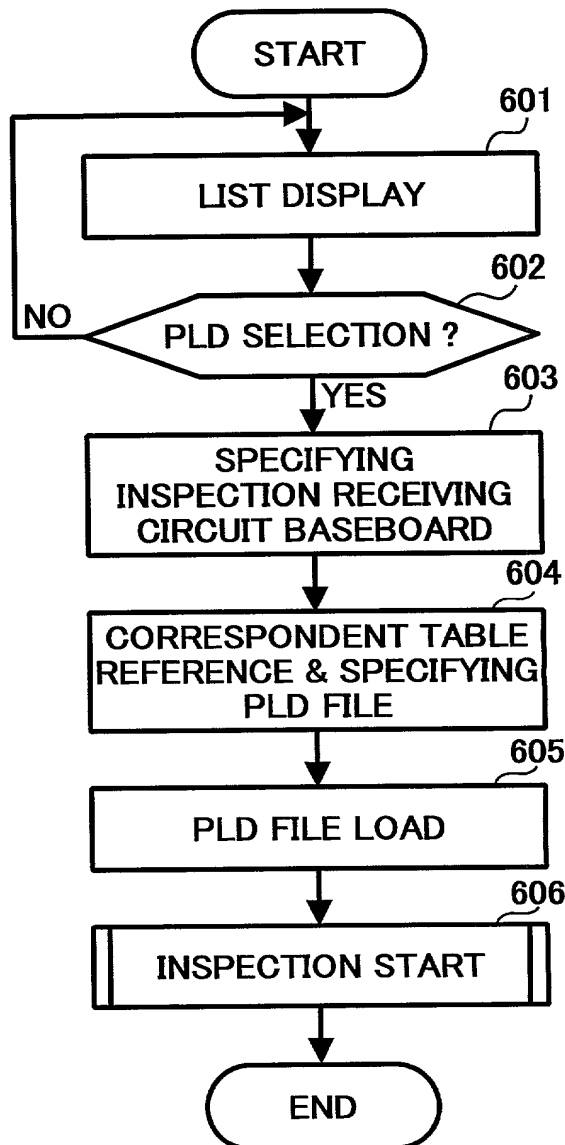


FIG. 18

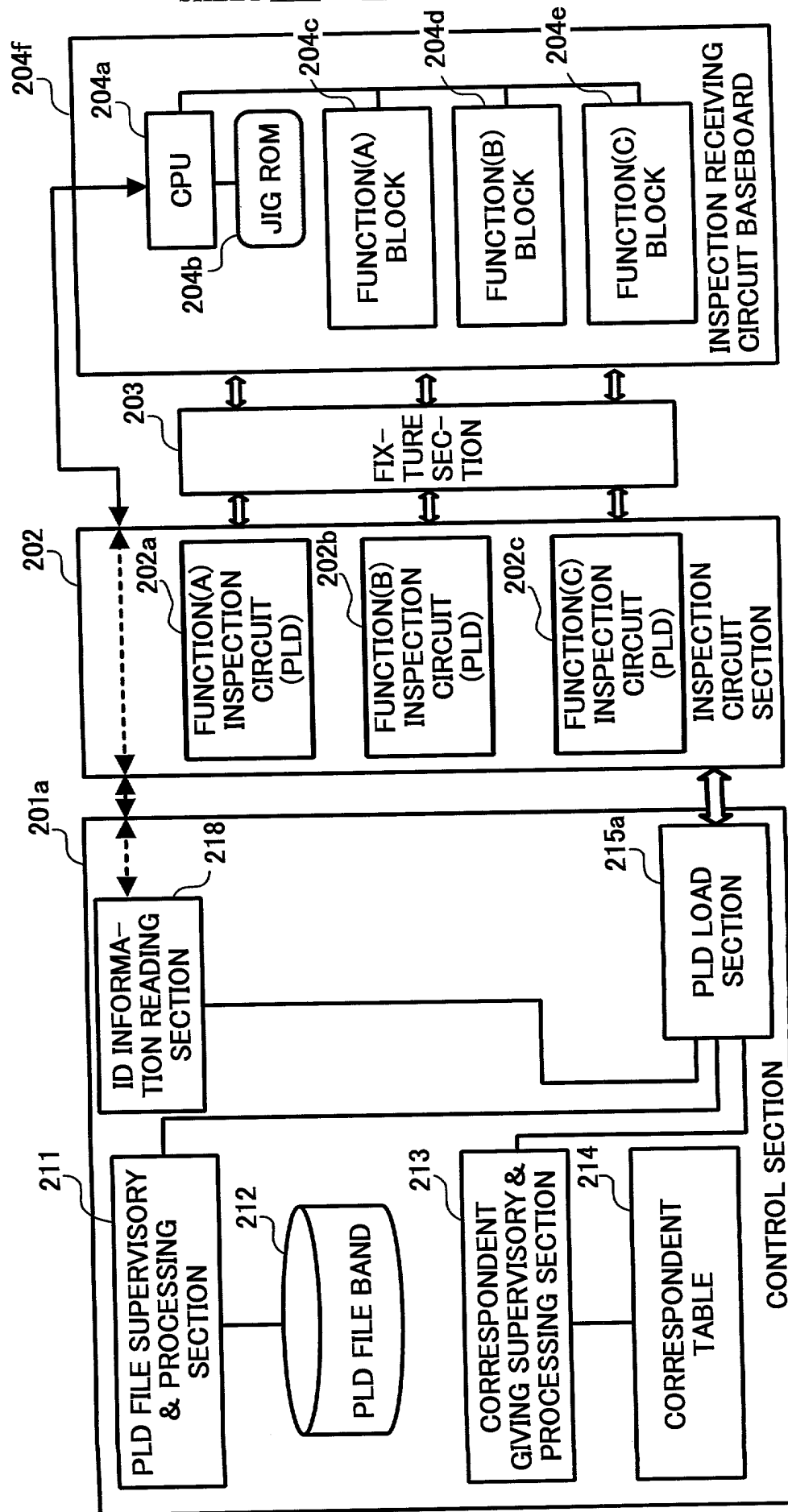


FIG. 19

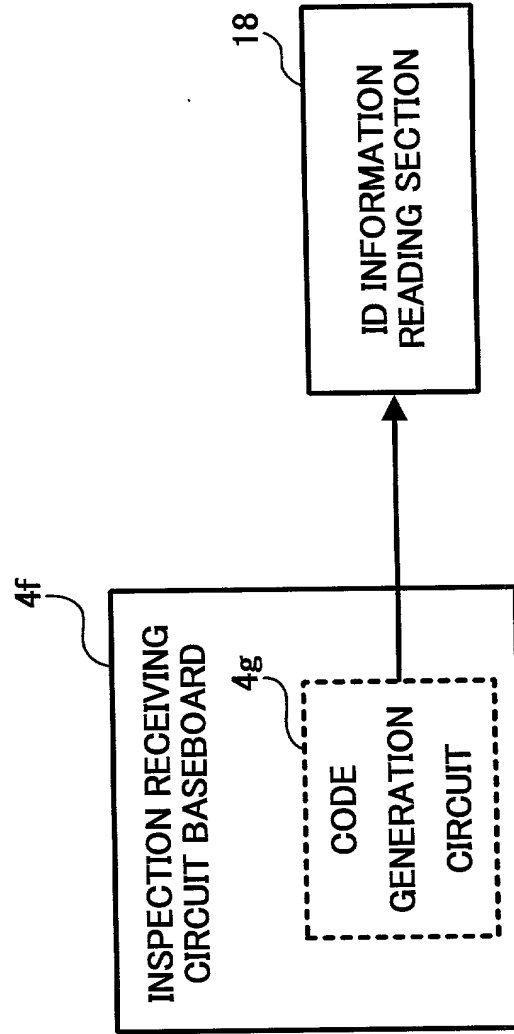


FIG. 20

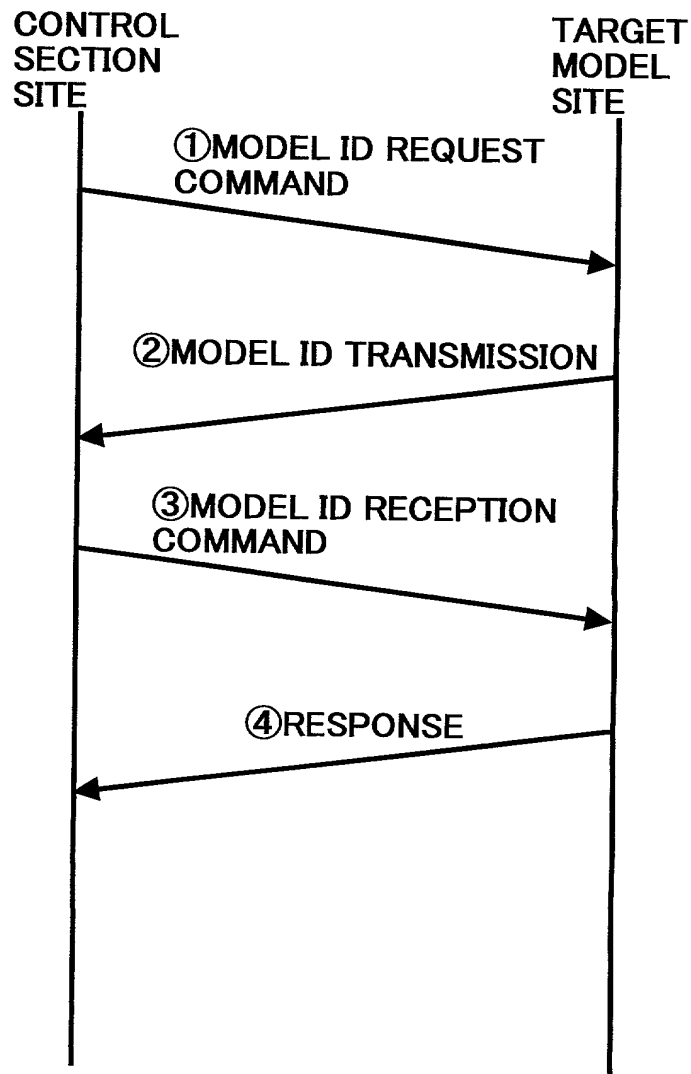


FIG. 21

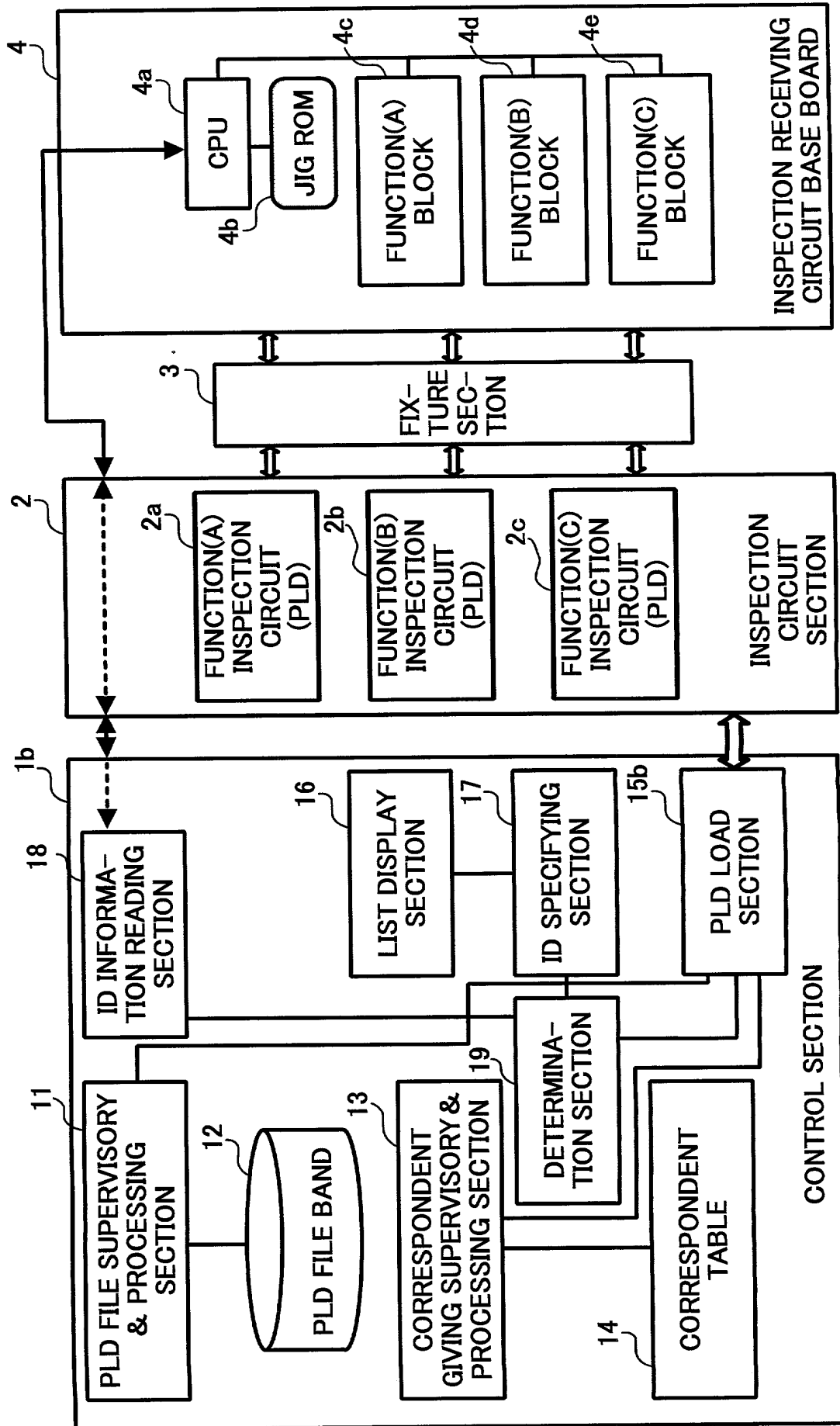


FIG. 22

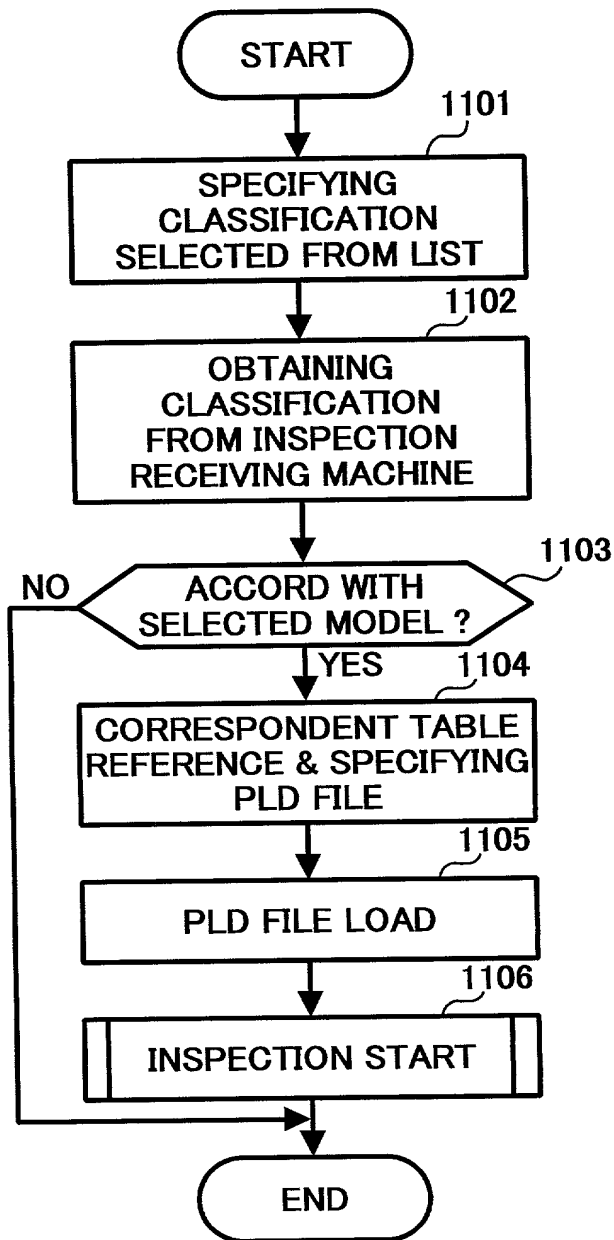


FIG. 23

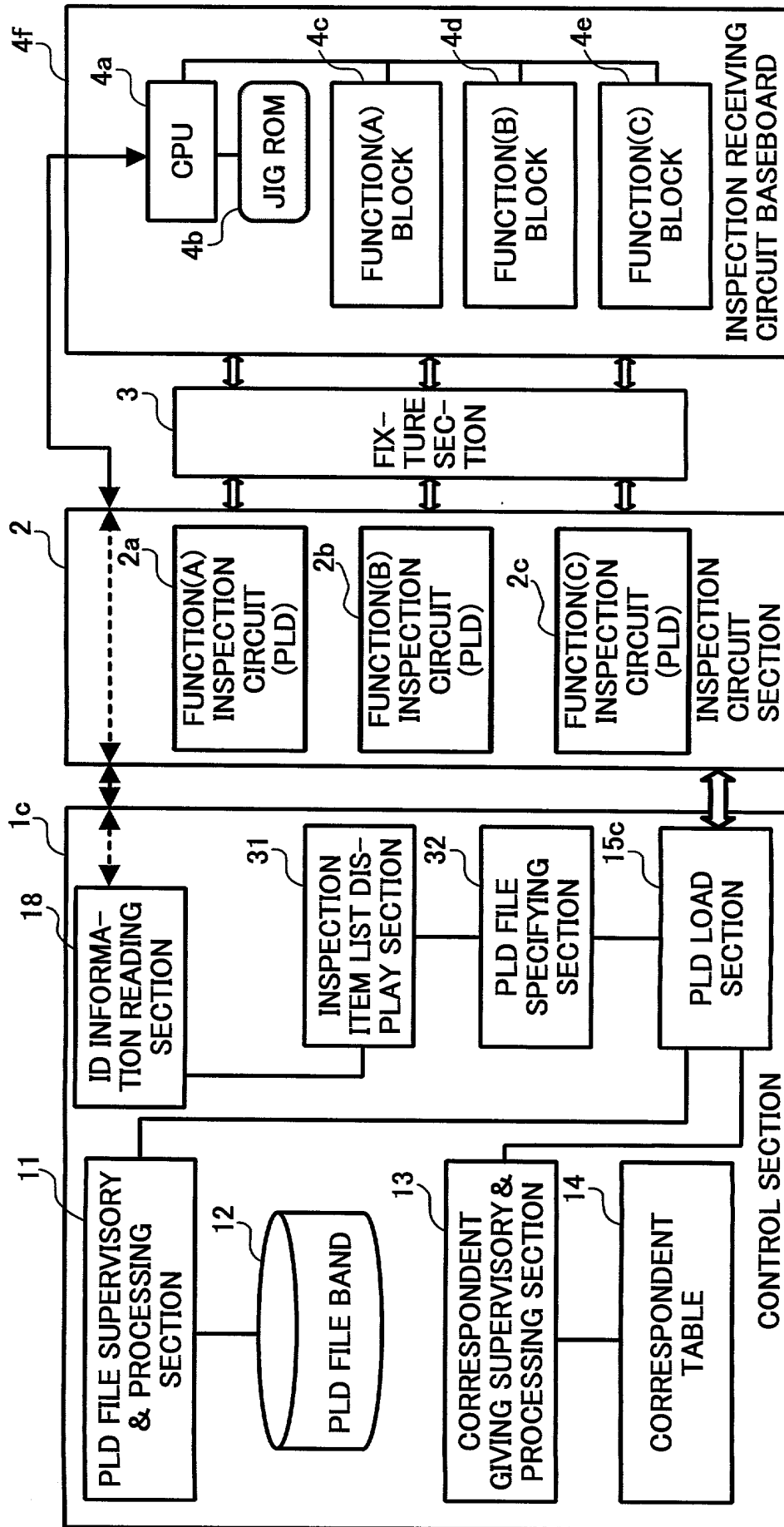


FIG. 24

31b

PLD LOAD

MAIN	SUB
<input type="checkbox"/> PLD_TAL	<input type="checkbox"/> SPLD1
<input type="checkbox"/> MPLD1	<input type="checkbox"/> SPLD2
<input type="checkbox"/> MPLD2	<input type="checkbox"/> SPLD3
<input type="checkbox"/> MPLD3	<input type="checkbox"/> SPLD4
<input type="checkbox"/> MPLD4	<input type="checkbox"/> SPLD5
<input type="checkbox"/> MPLD5	<input type="checkbox"/> SPLD6
<input type="checkbox"/> MPLD6	<input type="checkbox"/> SPLD7
<input type="checkbox"/> MPLD7	<input type="checkbox"/> SPLD8
<input type="checkbox"/> MPLD8	<input type="checkbox"/> SPLD9
<input type="checkbox"/> MPLD9	
<input type="checkbox"/> MPLD10	
<input type="checkbox"/> MPLD11	
<input type="checkbox"/> MPLD12	
<input type="checkbox"/> MPLD13	

CHECK ALL

START(F5)

CLOSE

FIG. 25

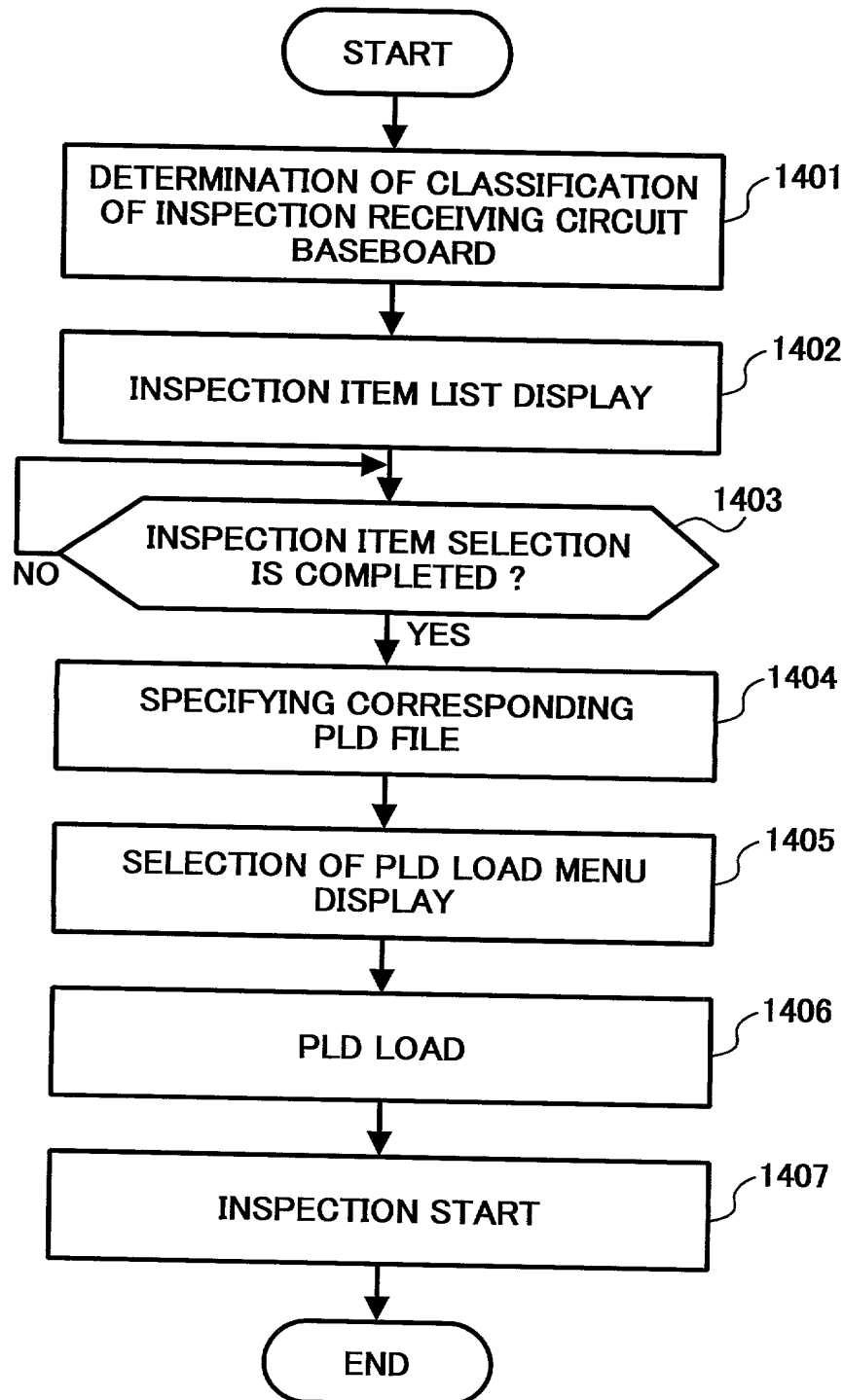


FIG. 26

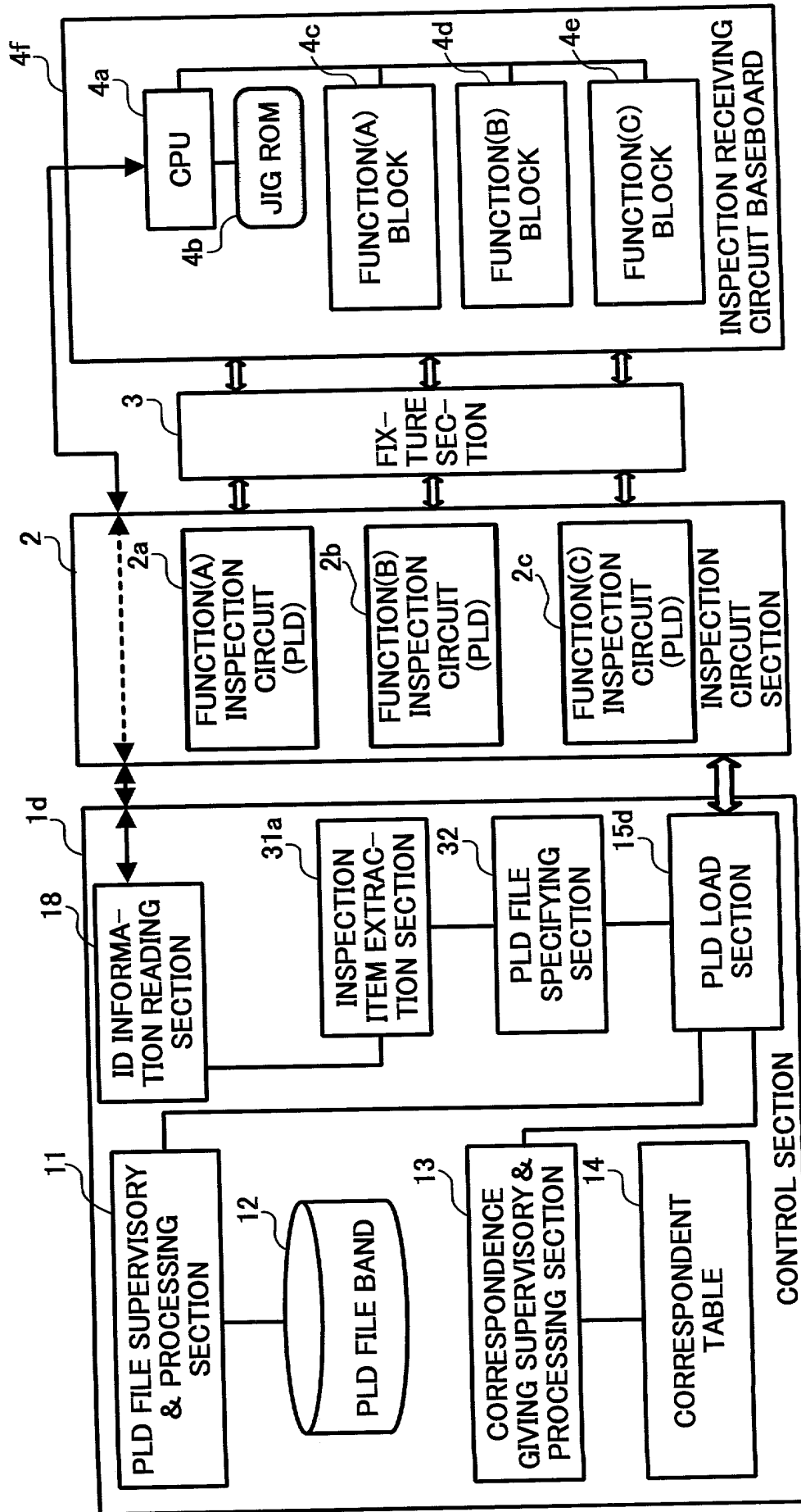


FIG. 27

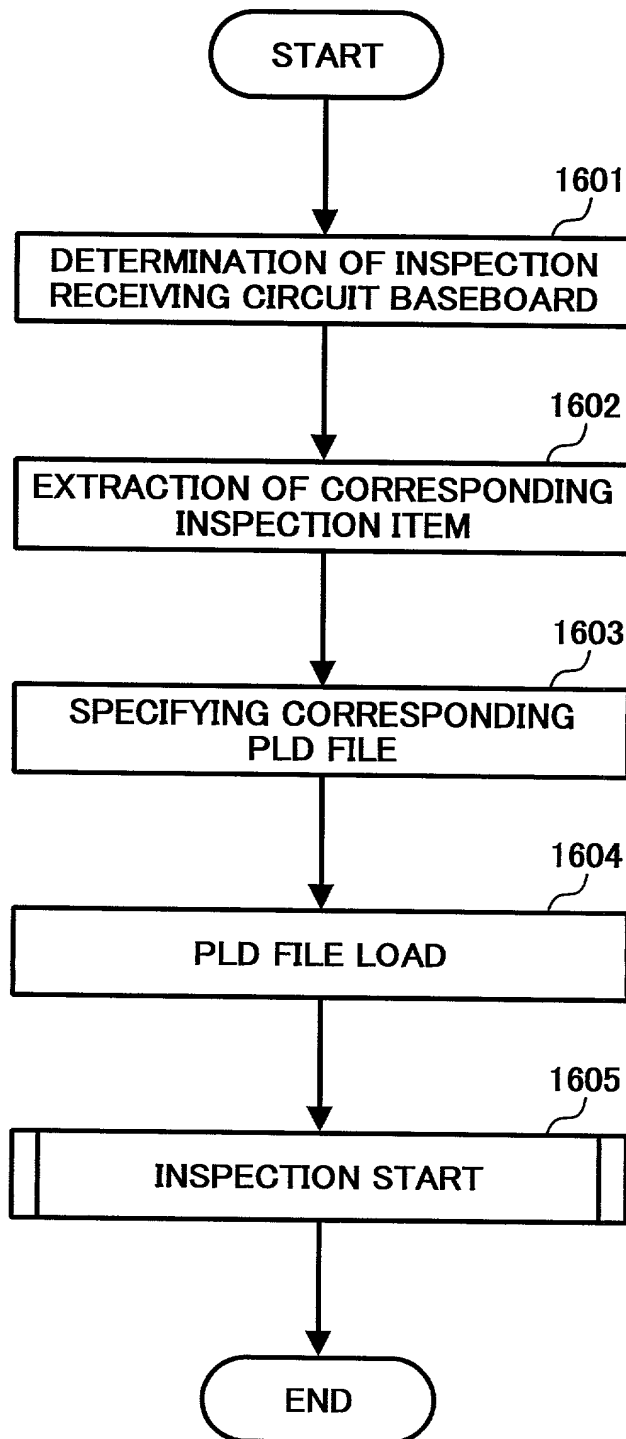


FIG. 28

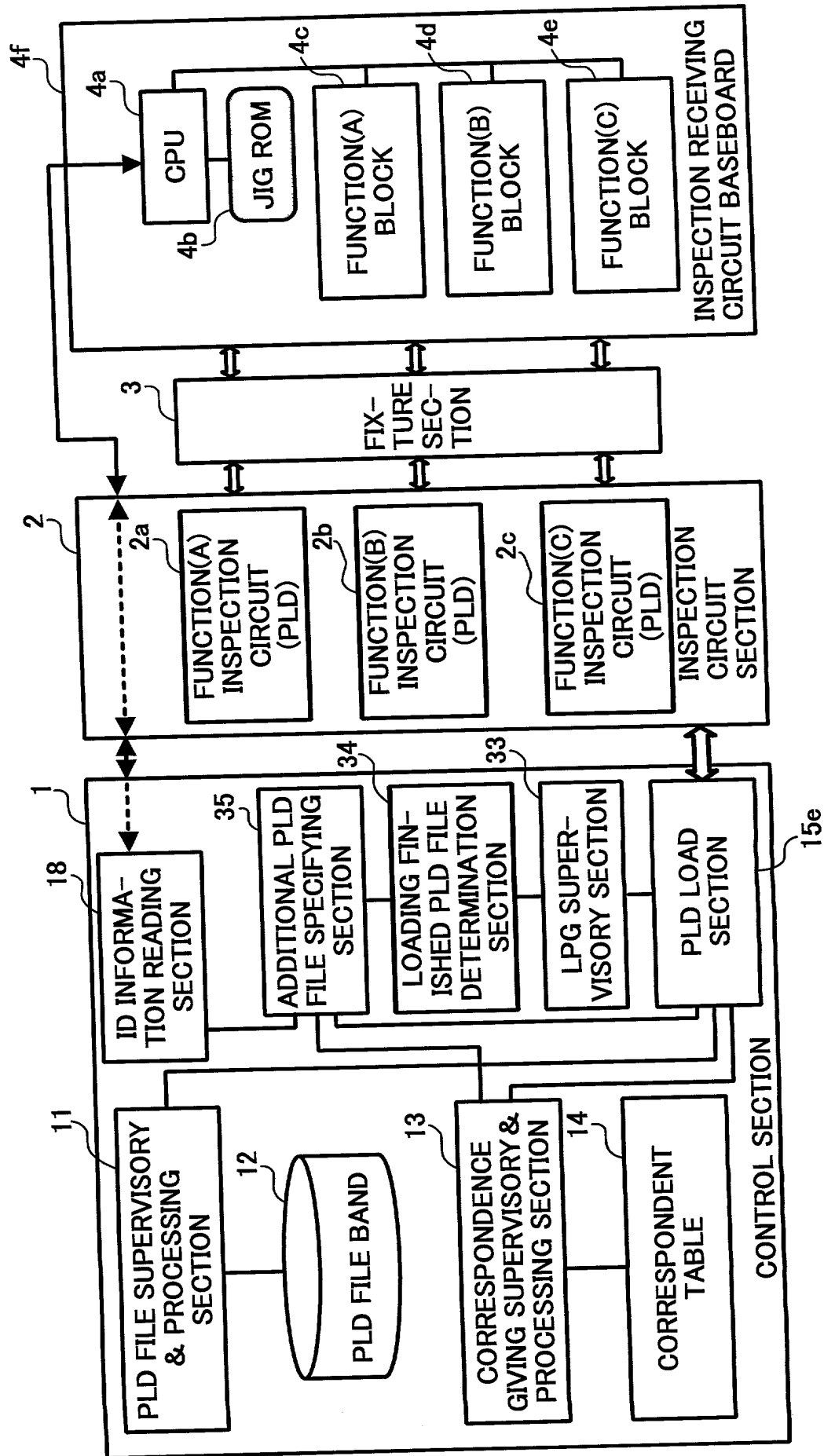


FIG. 29

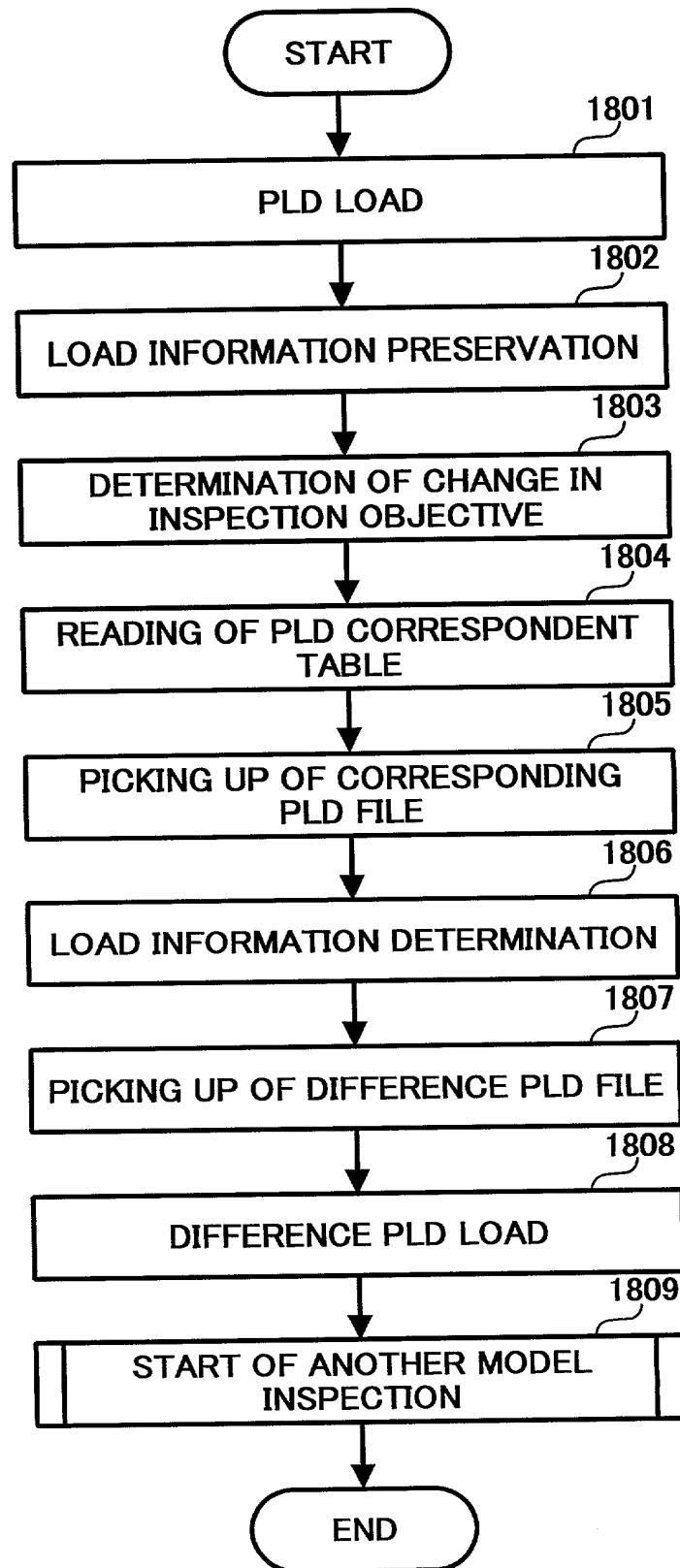


FIG. 30

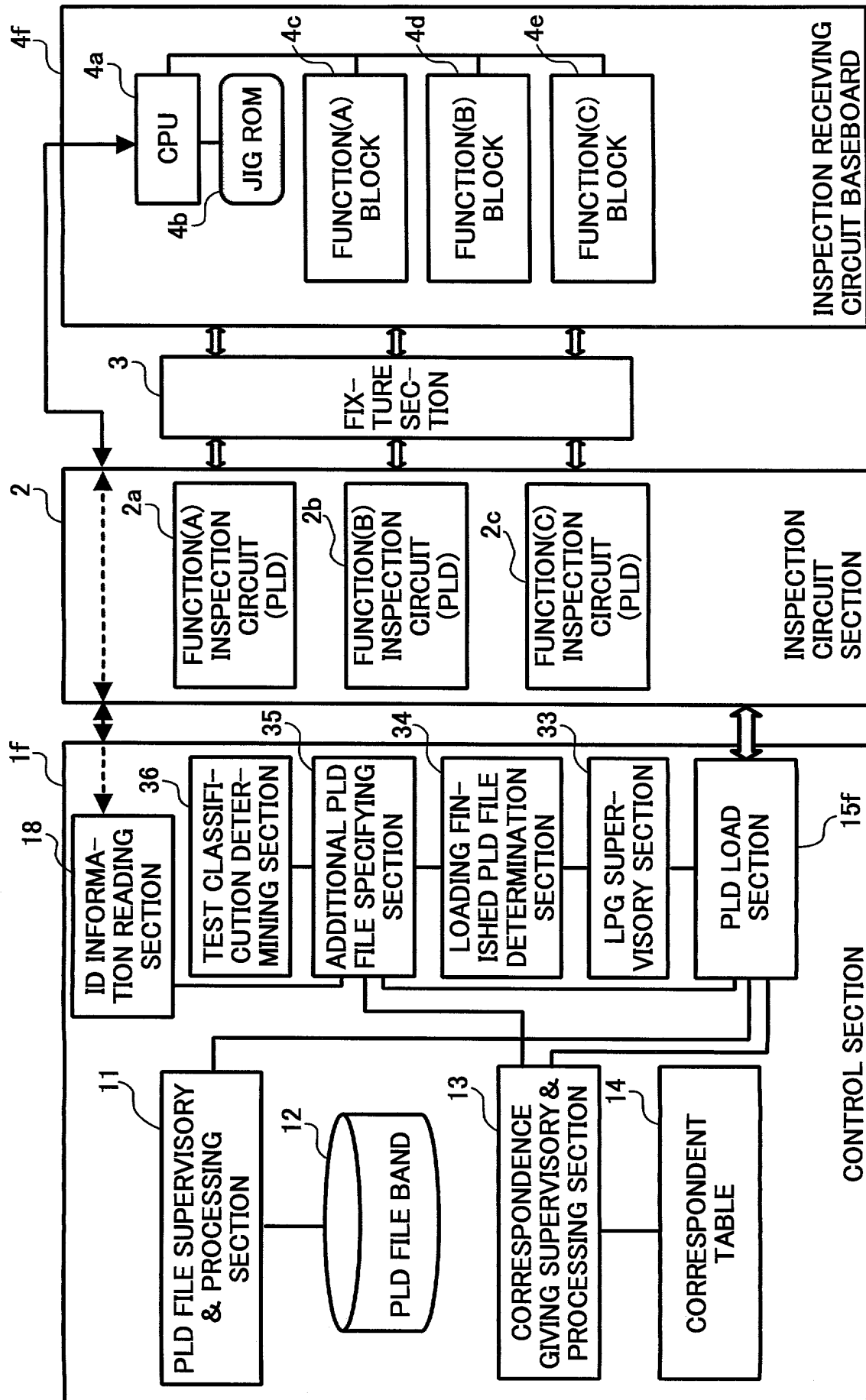


FIG. 31

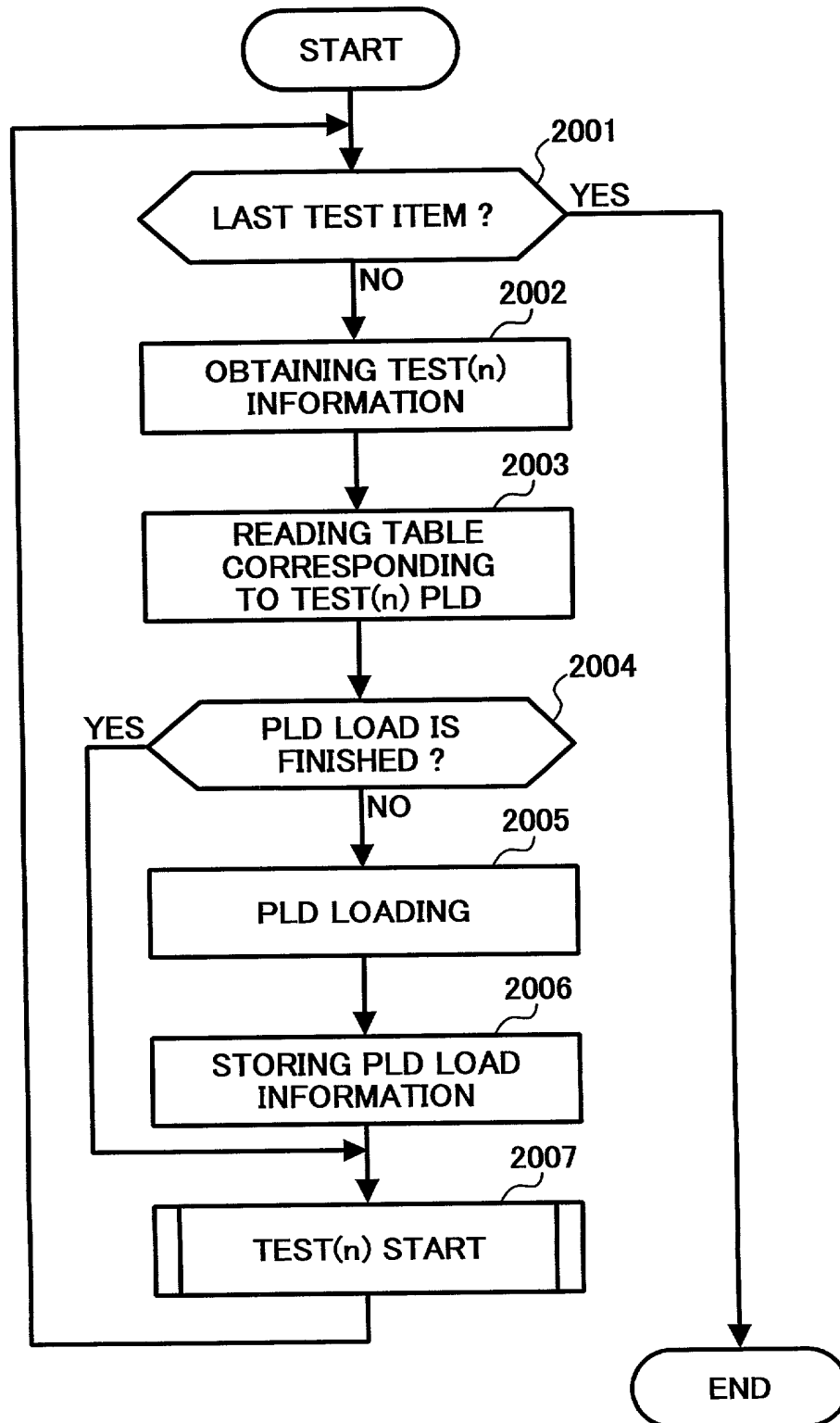


FIG. 32

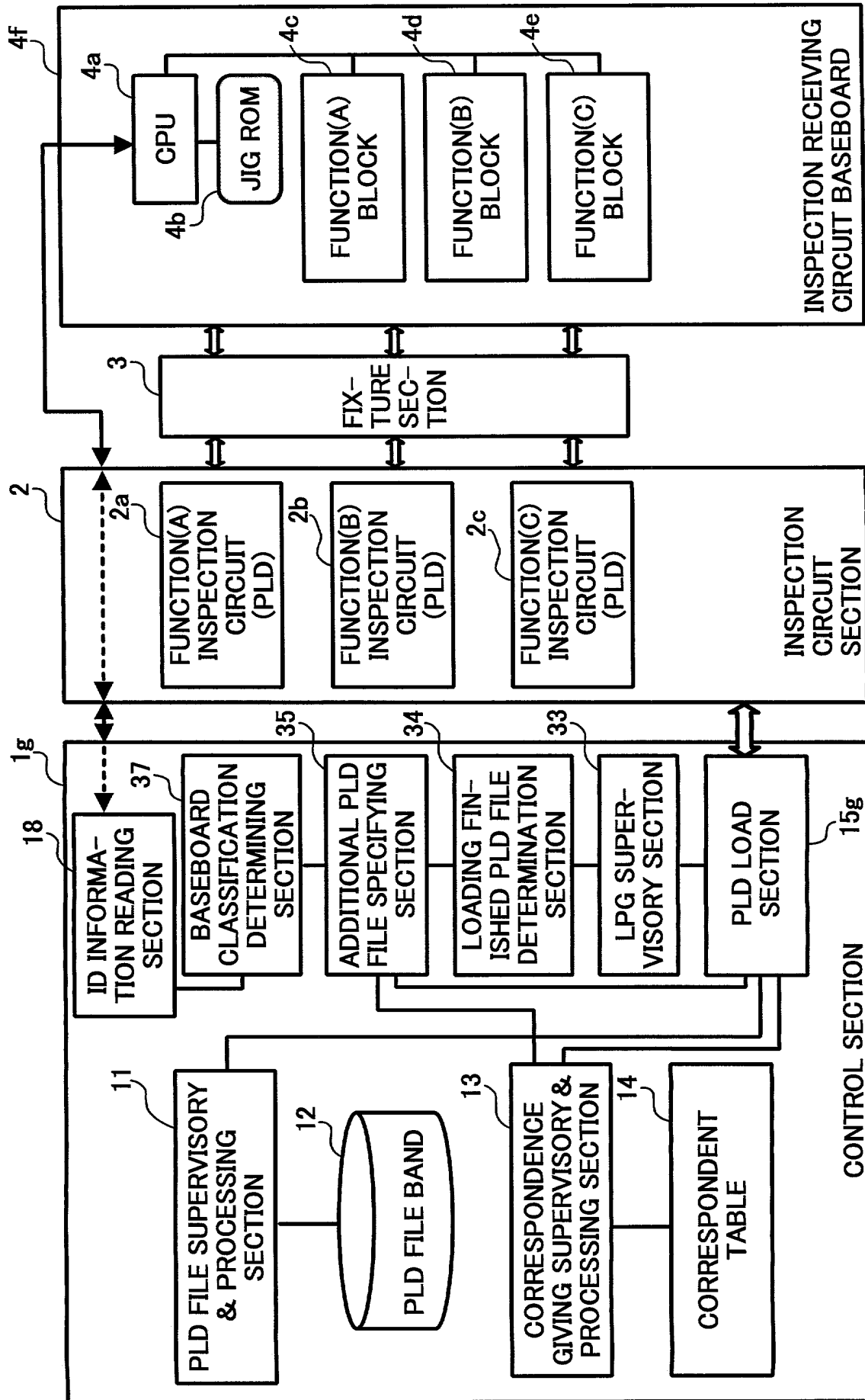


FIG. 33

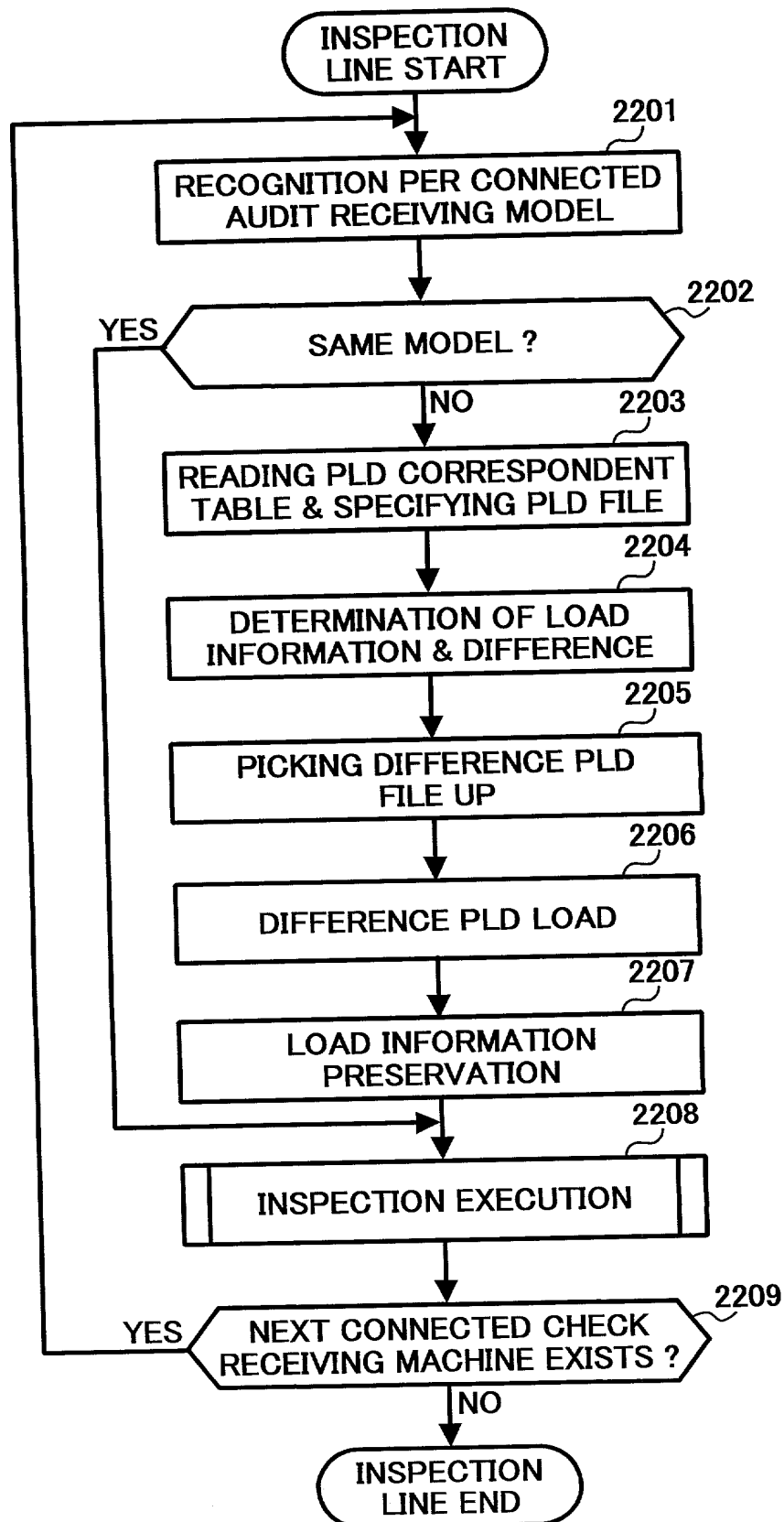


FIG. 34

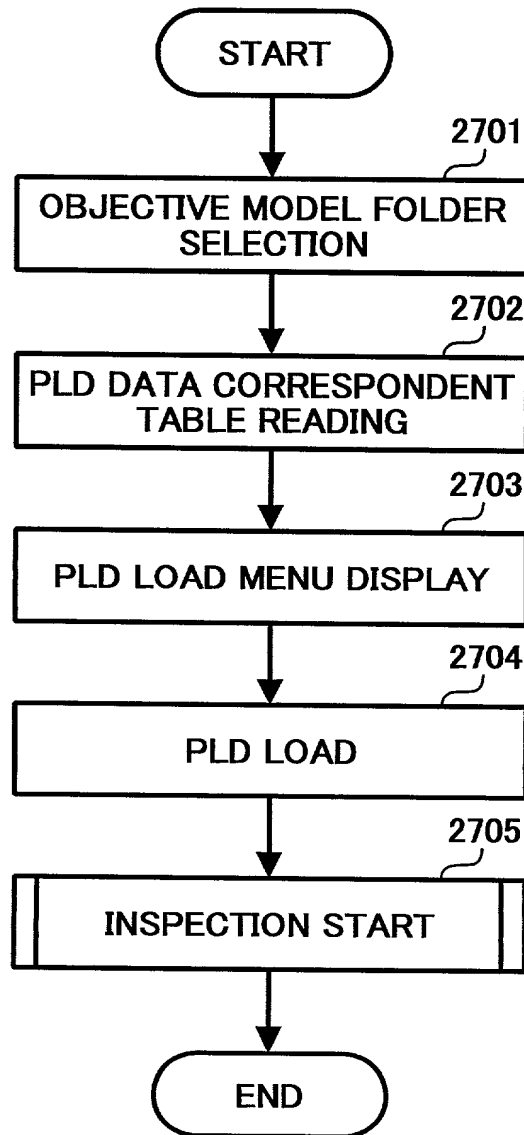


FIG. 35

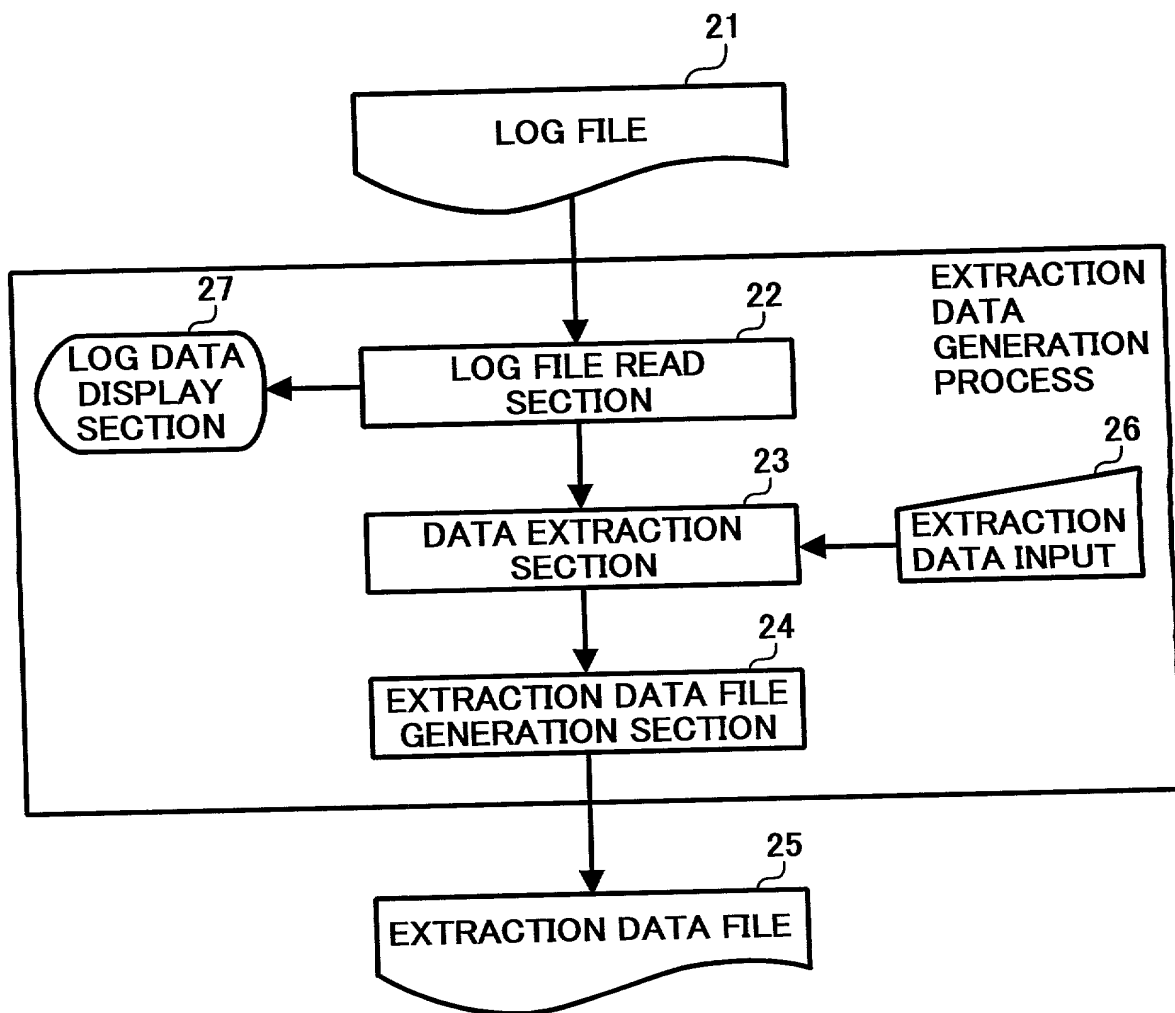


FIG. 36

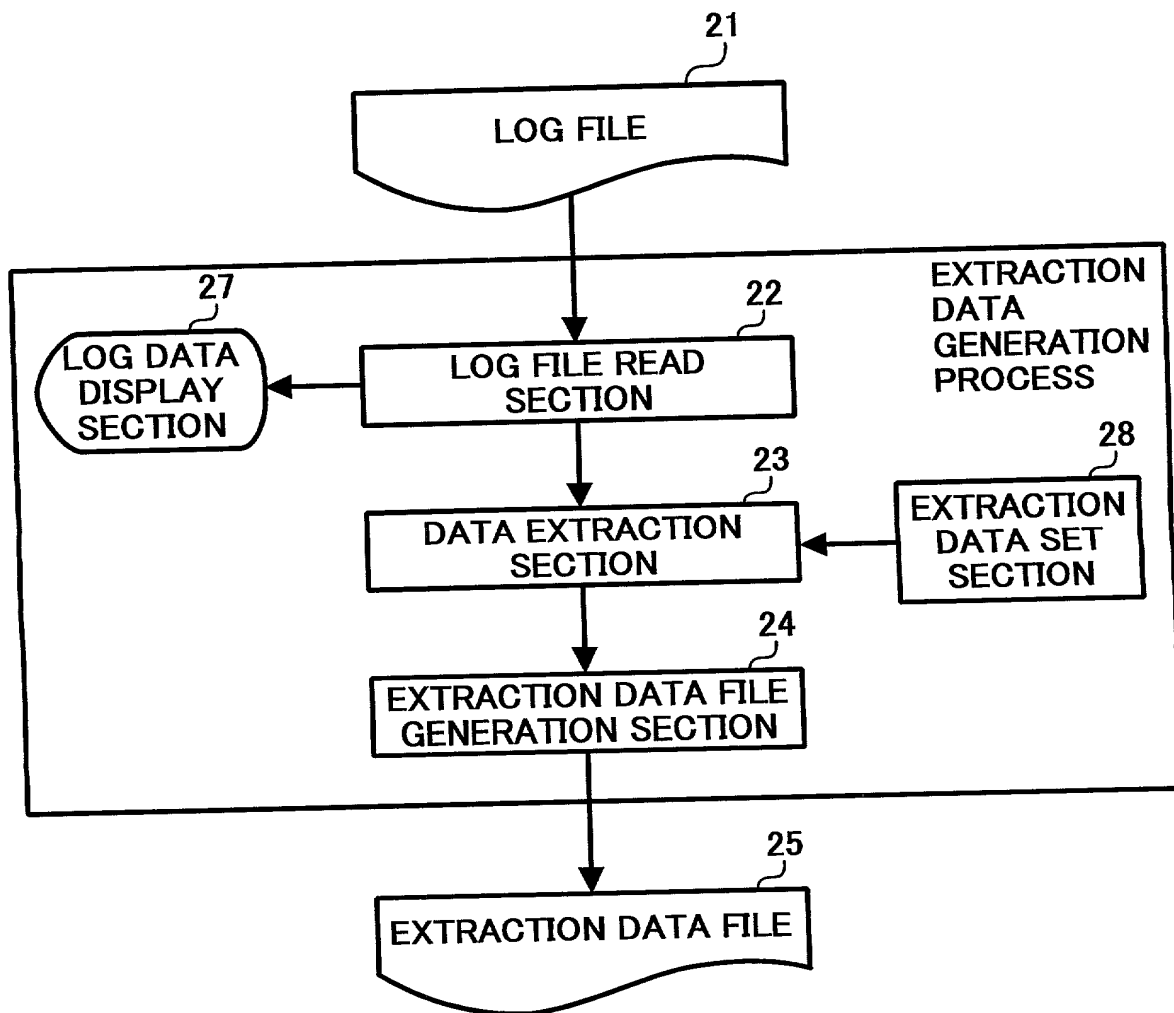


FIG. 37A

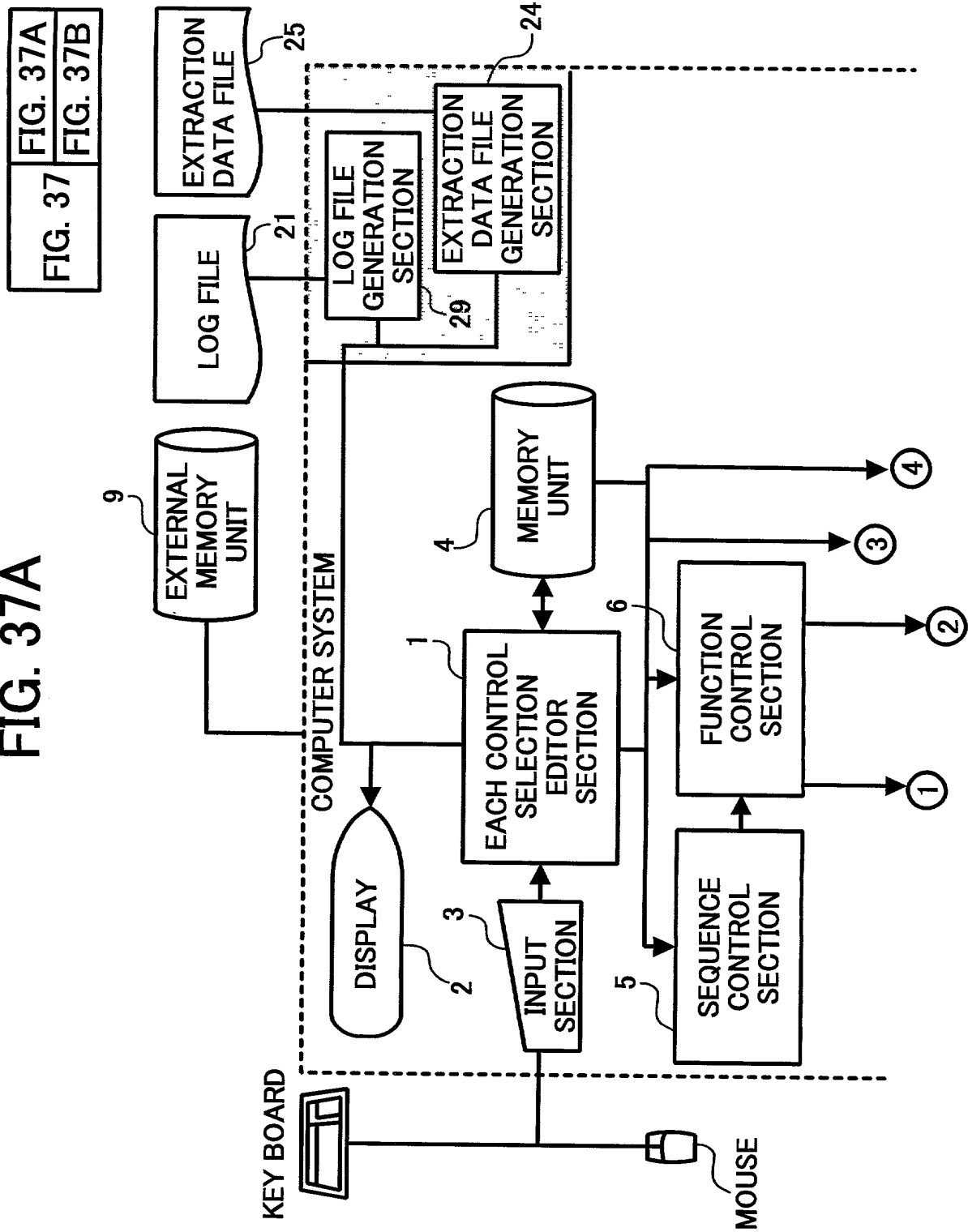
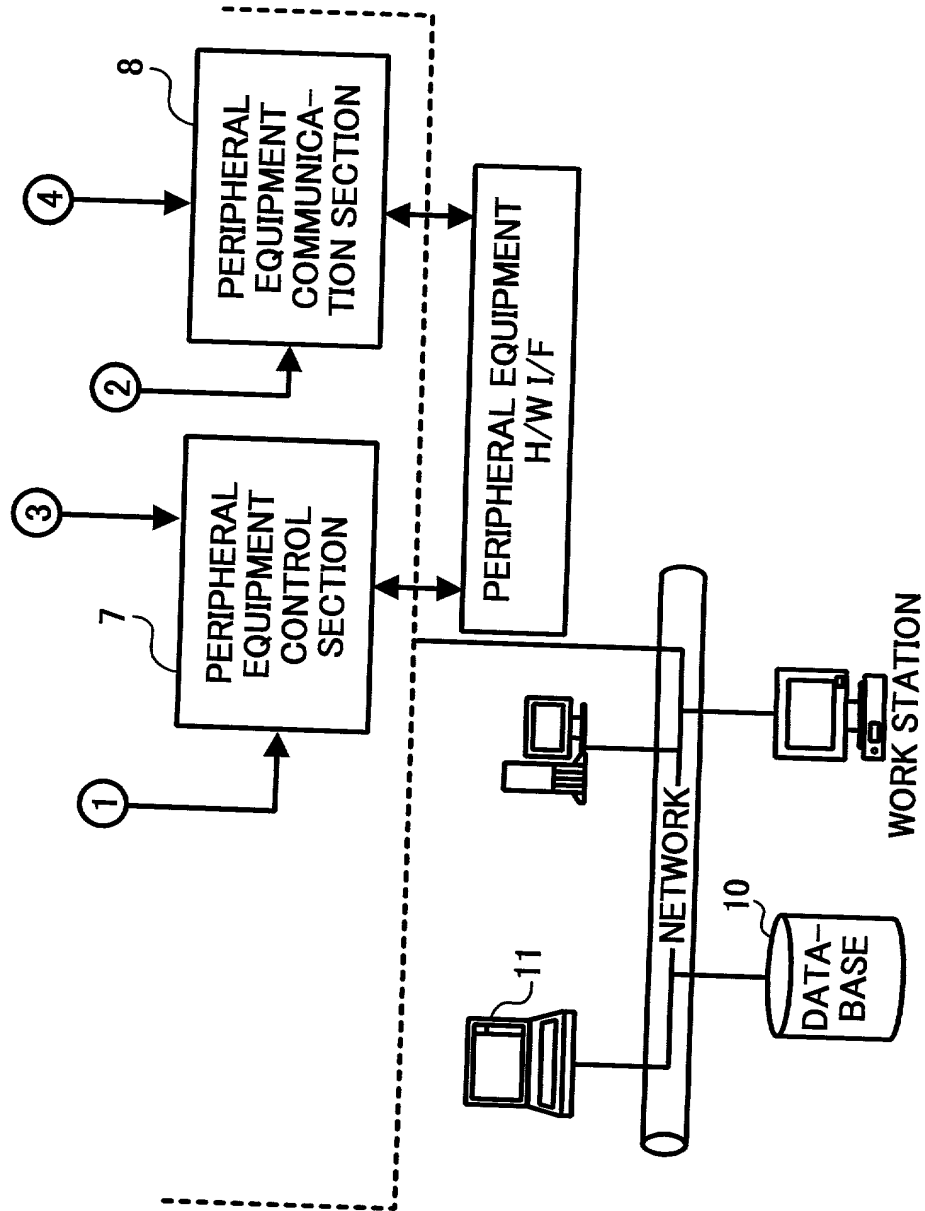


FIG. 37
FIG. 37A
FIG. 37B

FIG. 37B



[illegible][illegible]

FIG. 39

FILE EDIT TOOLS HELP

	RESULT	NO COUNT	TESTS DATA	TESTS DATA
PCB 1	PASS		2.012	0xFF93DA41
PCB 2	PASS		2.010	0xFF93C72B
PCB 3	FILE	3	2.005	0xFF41DB18
PCB 4	PASS		2.011	0xFF93DAF6
PCB 5	PASS		2.010	0xFF93EE1C
PCB 6	PASS		2.013	0xFF93DB28
PCB 7	FILE	5	2.015	0xFFA5C4C2

OK

SELECTION OF COLUMN
HAVING EXTRACTION INFOR-
MATION BY CLICKING MOUSE

FIG. 40

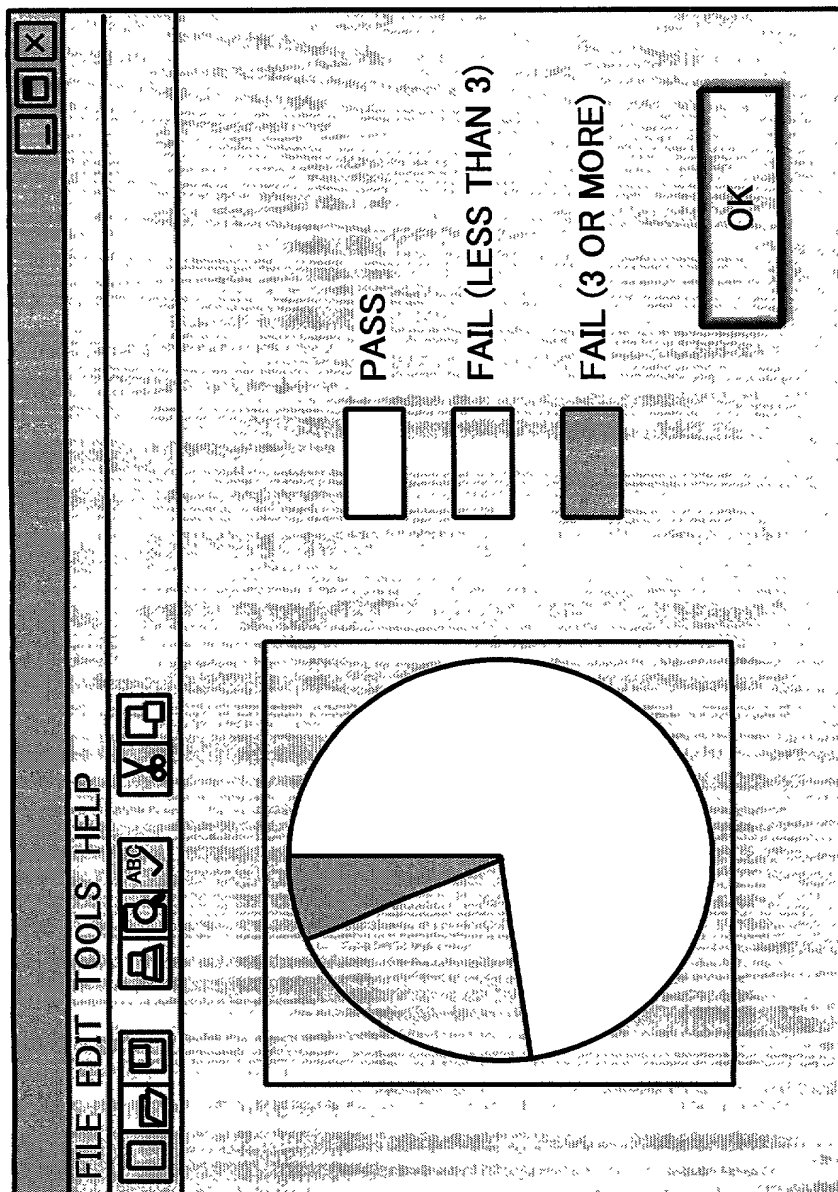


FIG. 41

FIG. 41

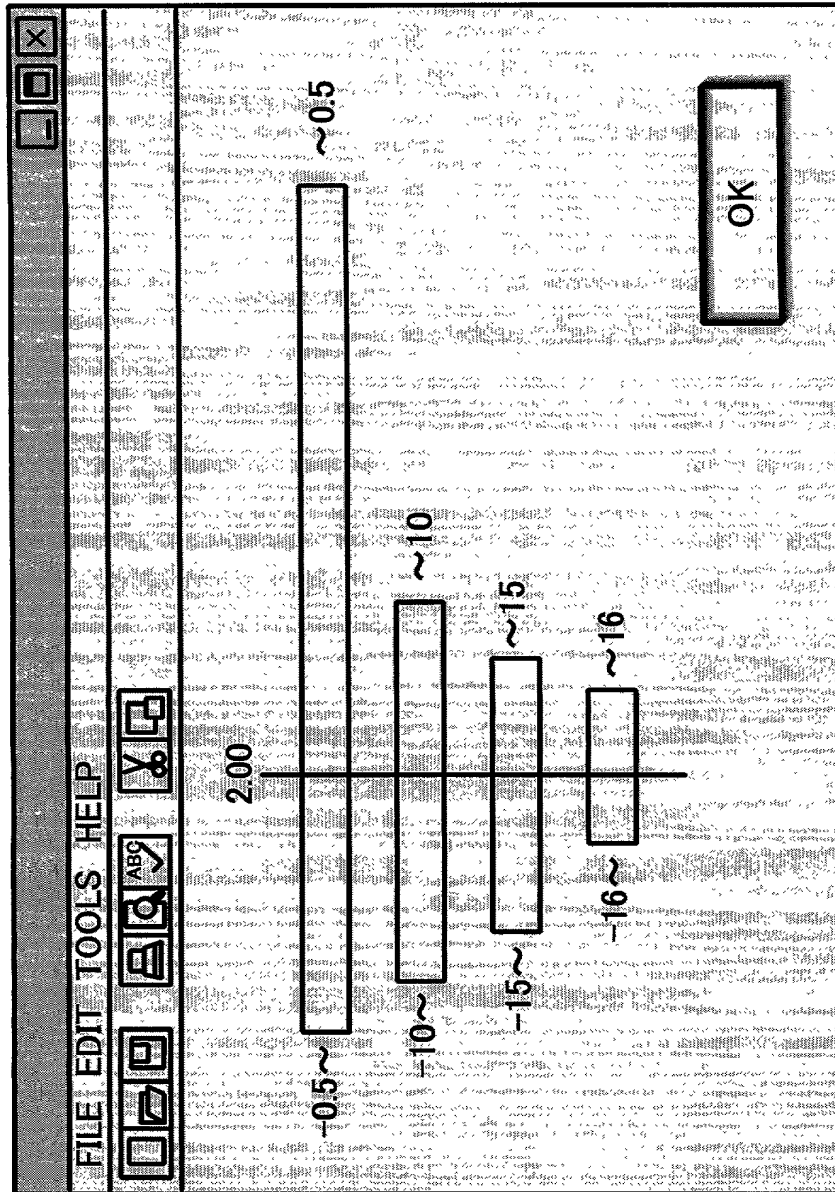


FIG. 42

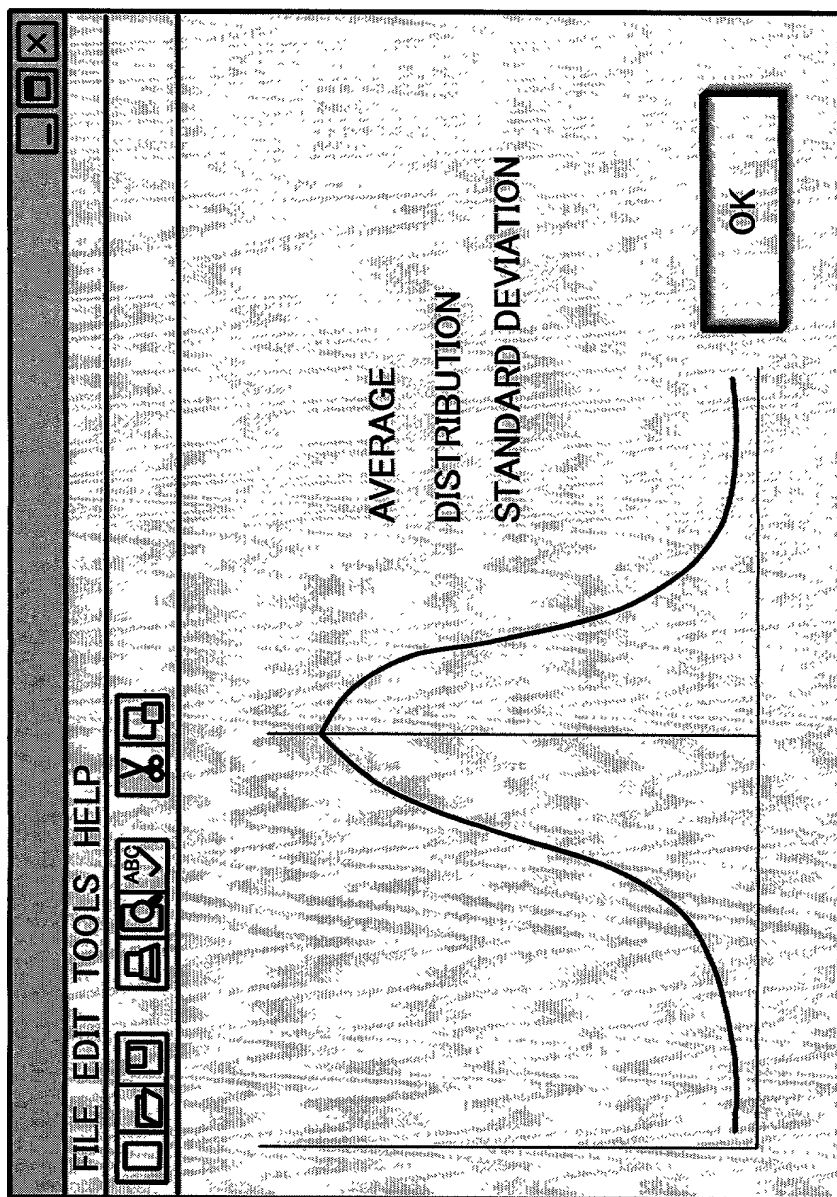


FIG. 43

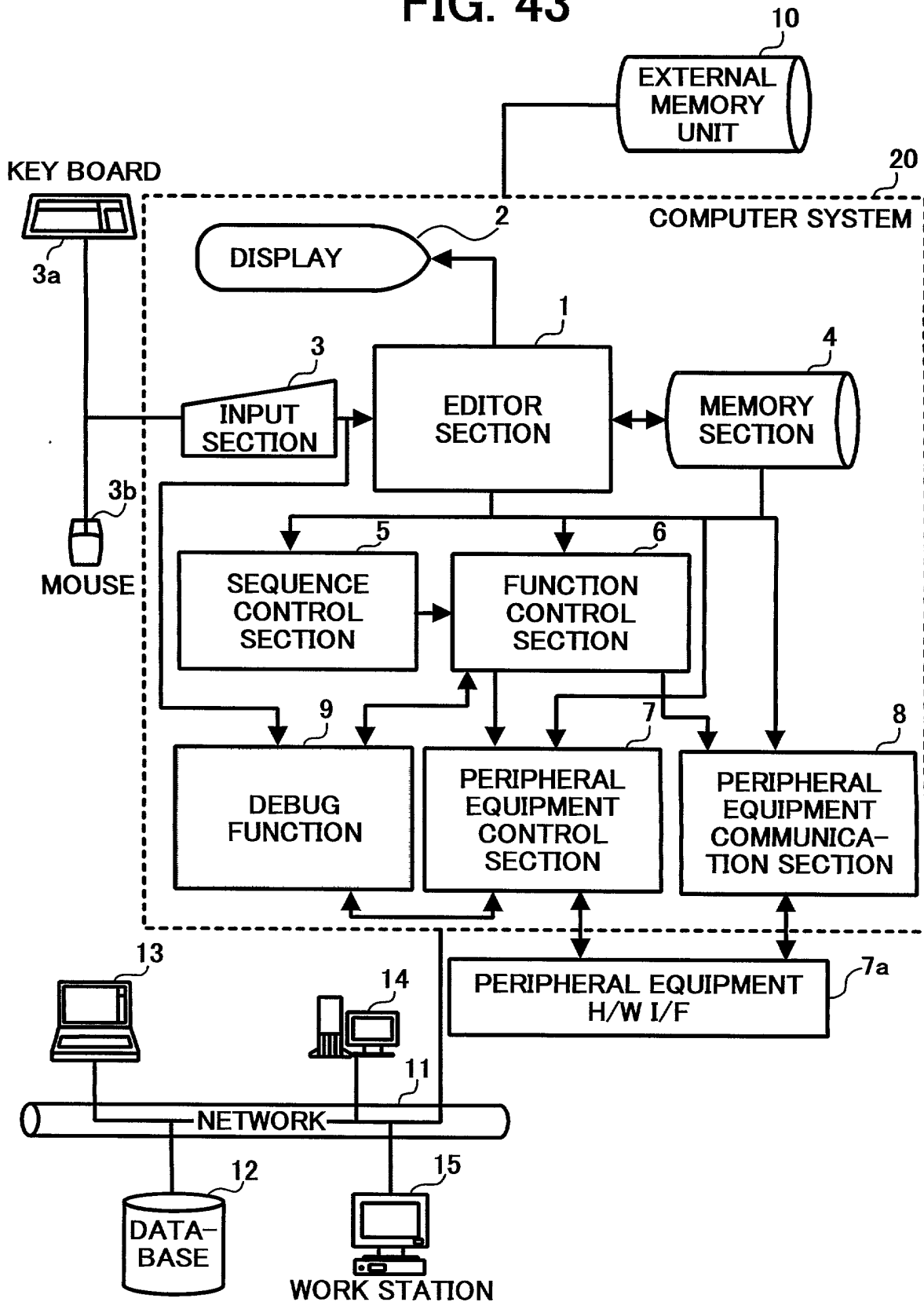


FIG. 44

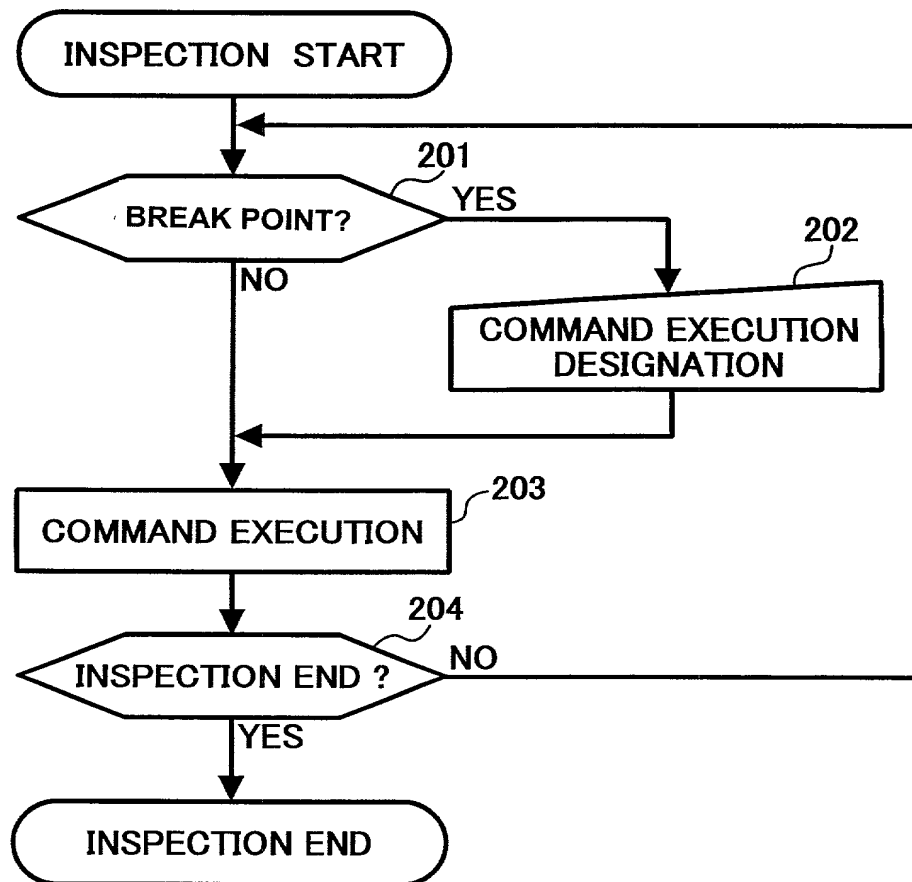


FIG. 45

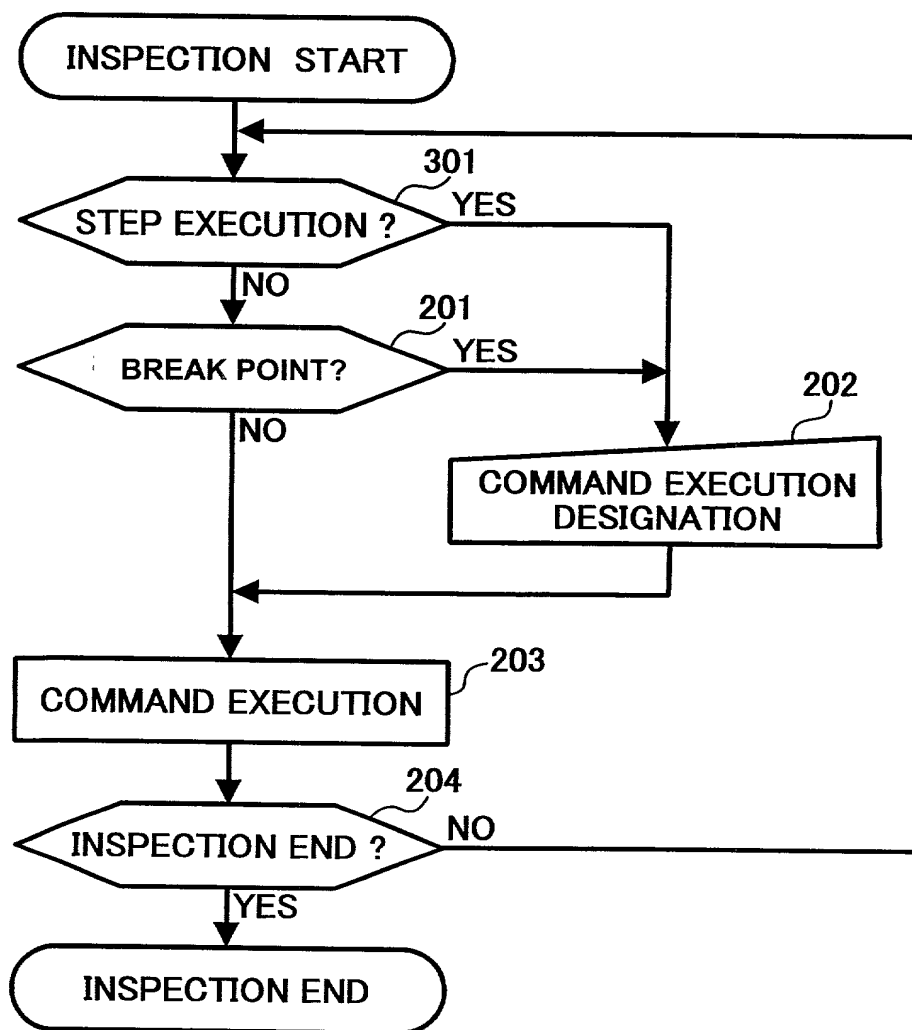


FIG. 47

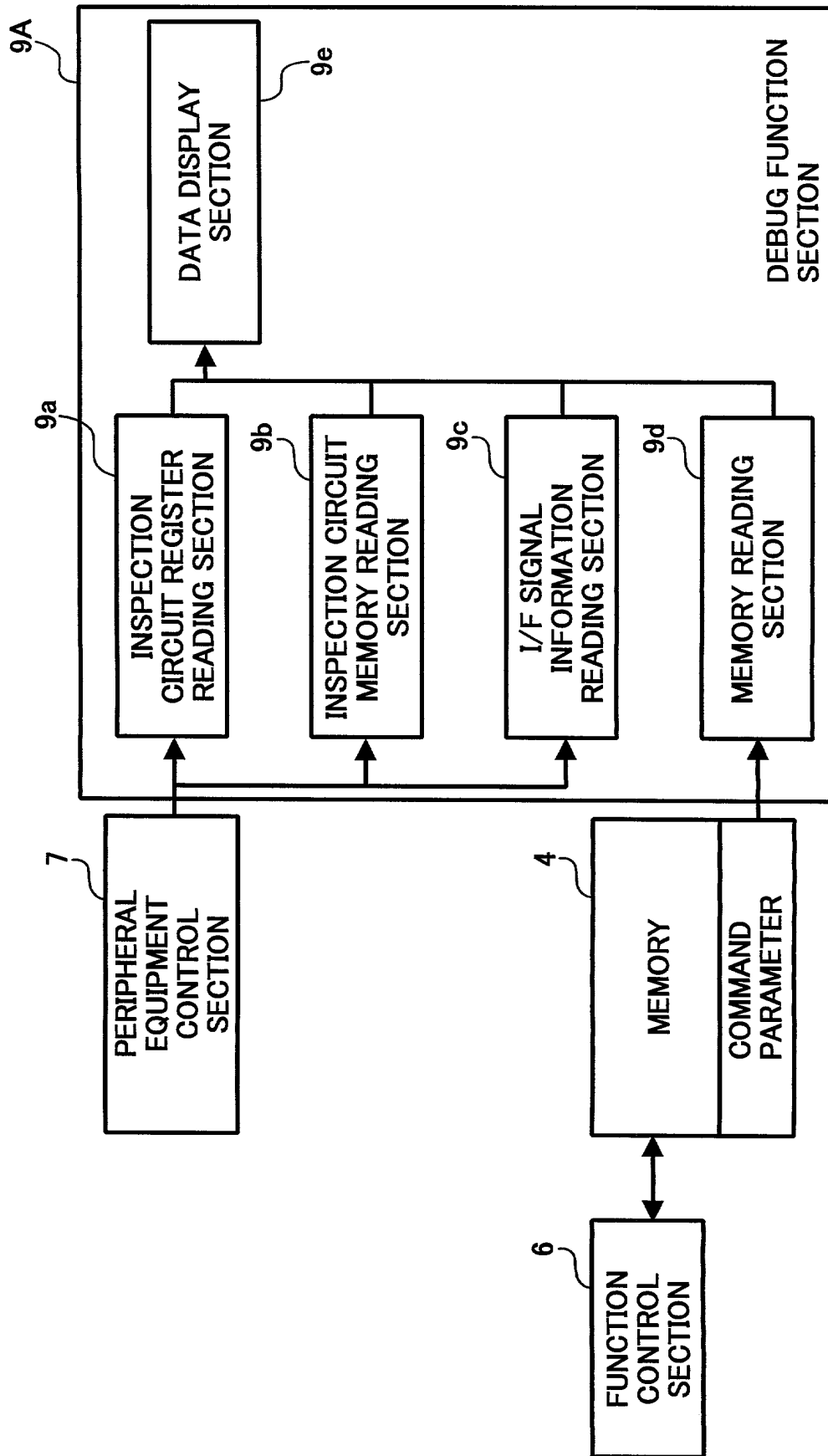


FIG. 48

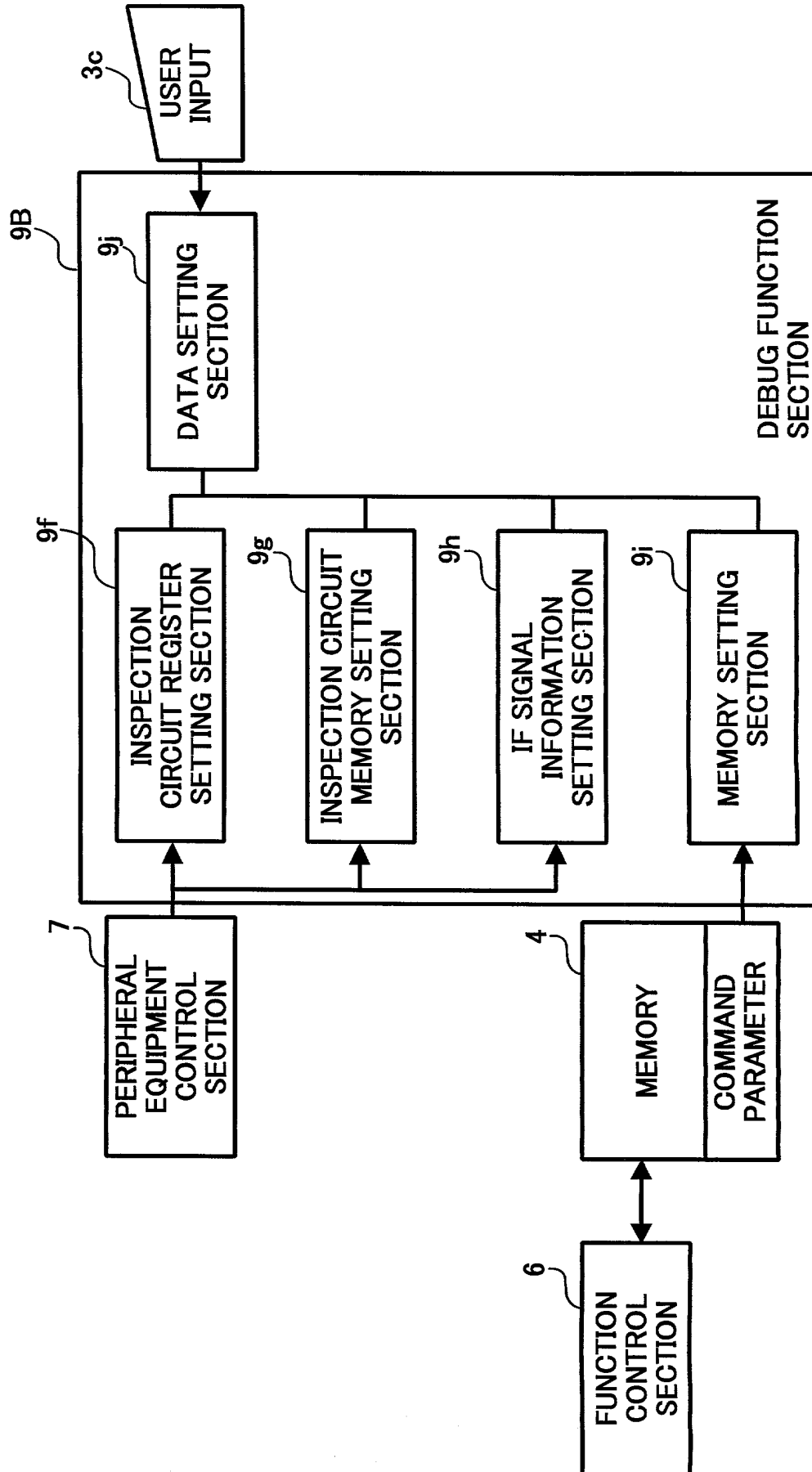


FIG. 49

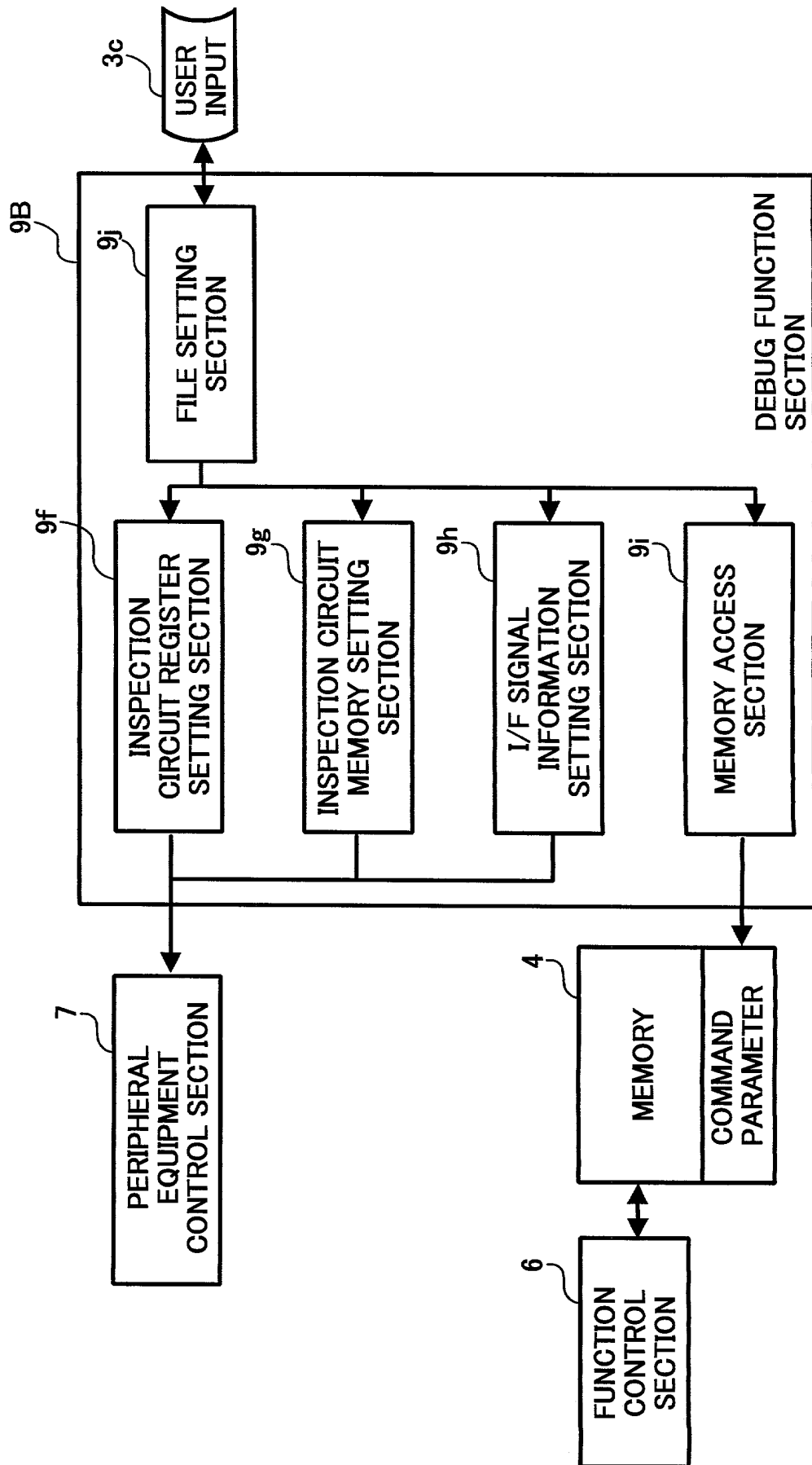


FIG. 50

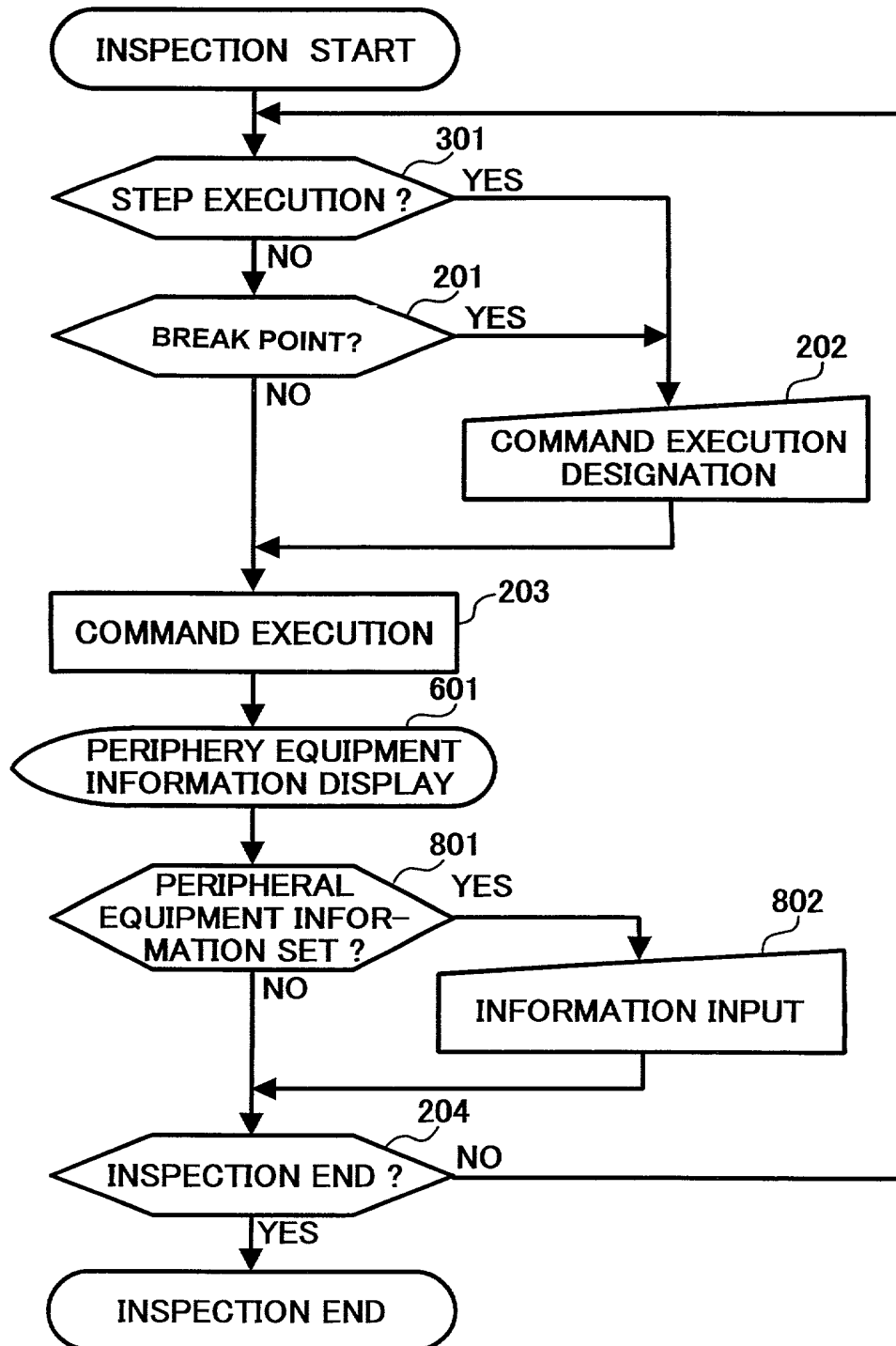


FIG. 51

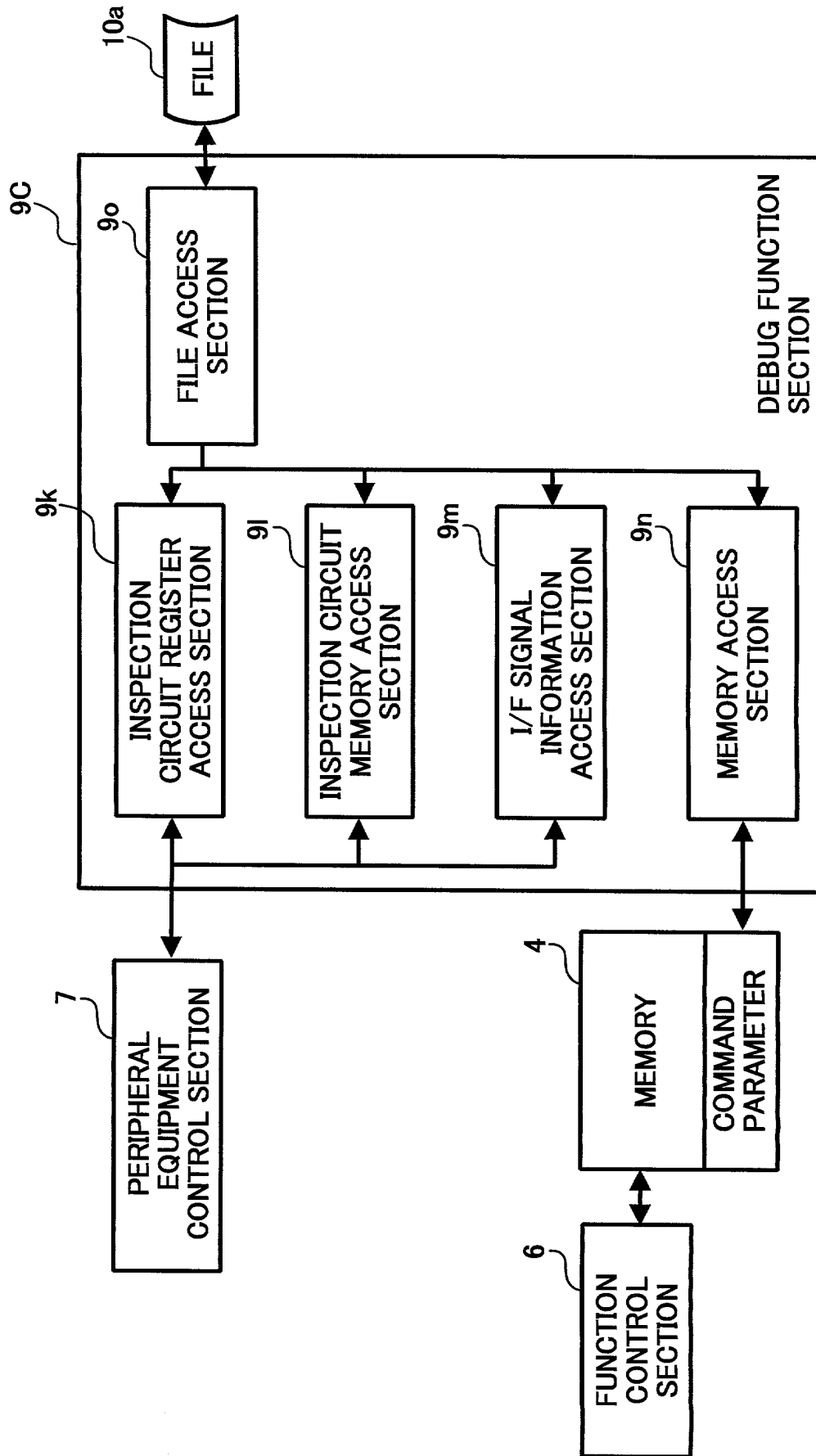


FIG. 52

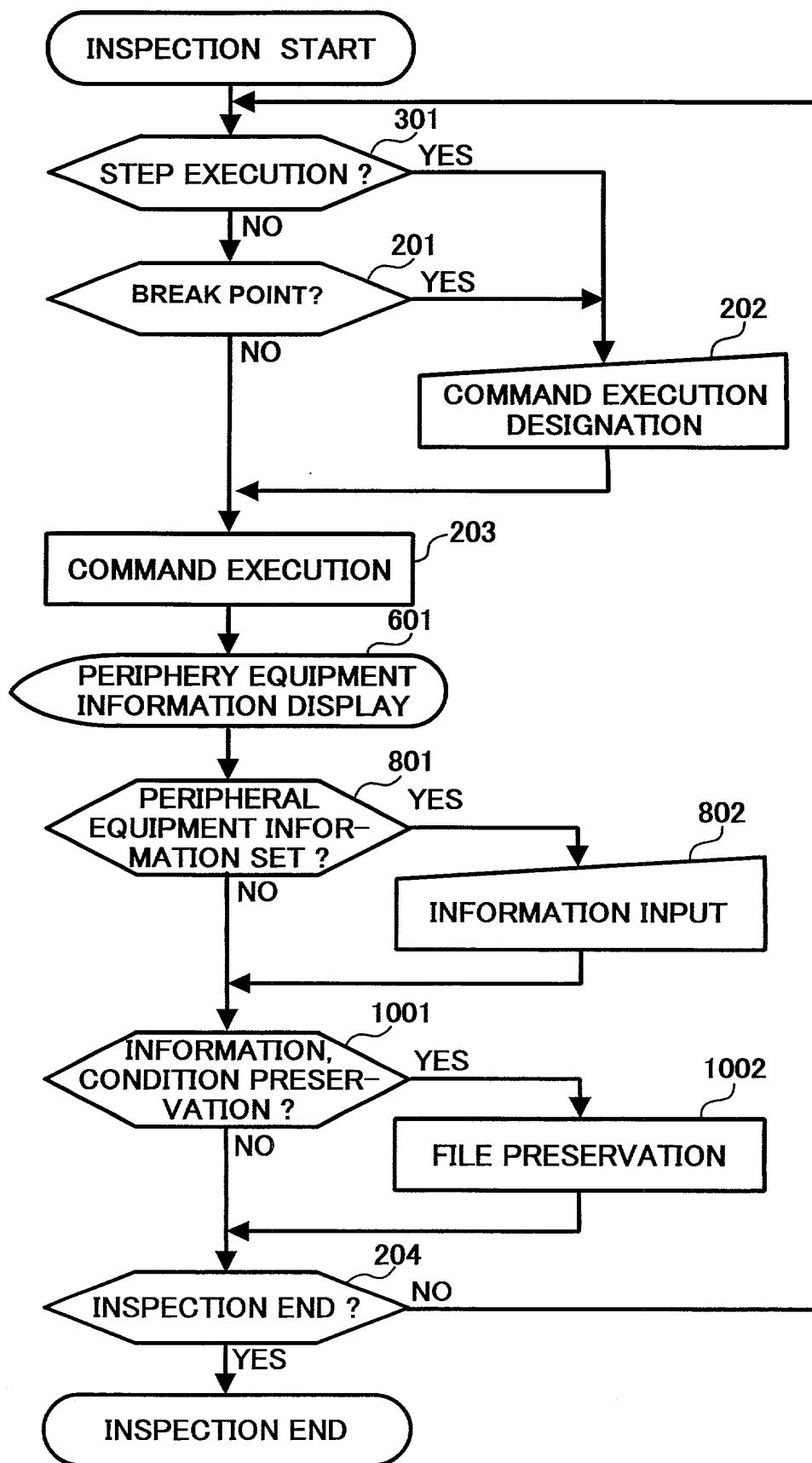


FIG. 53A

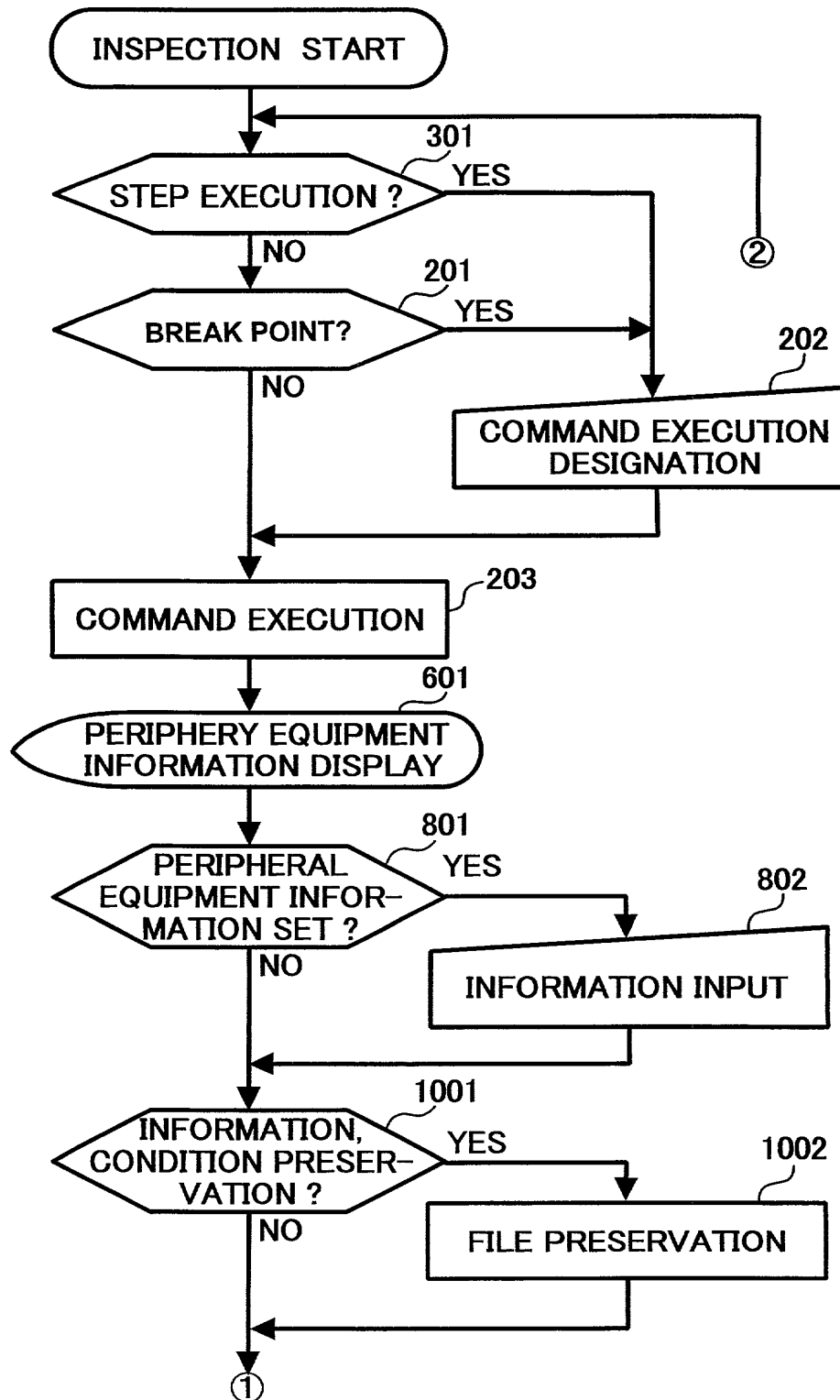


FIG. 53B

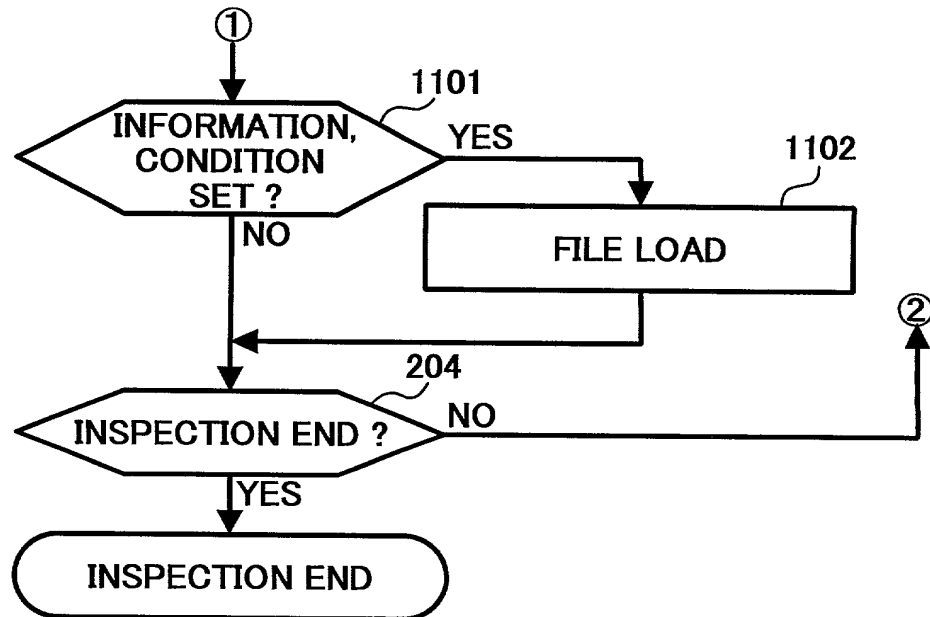


FIG. 54

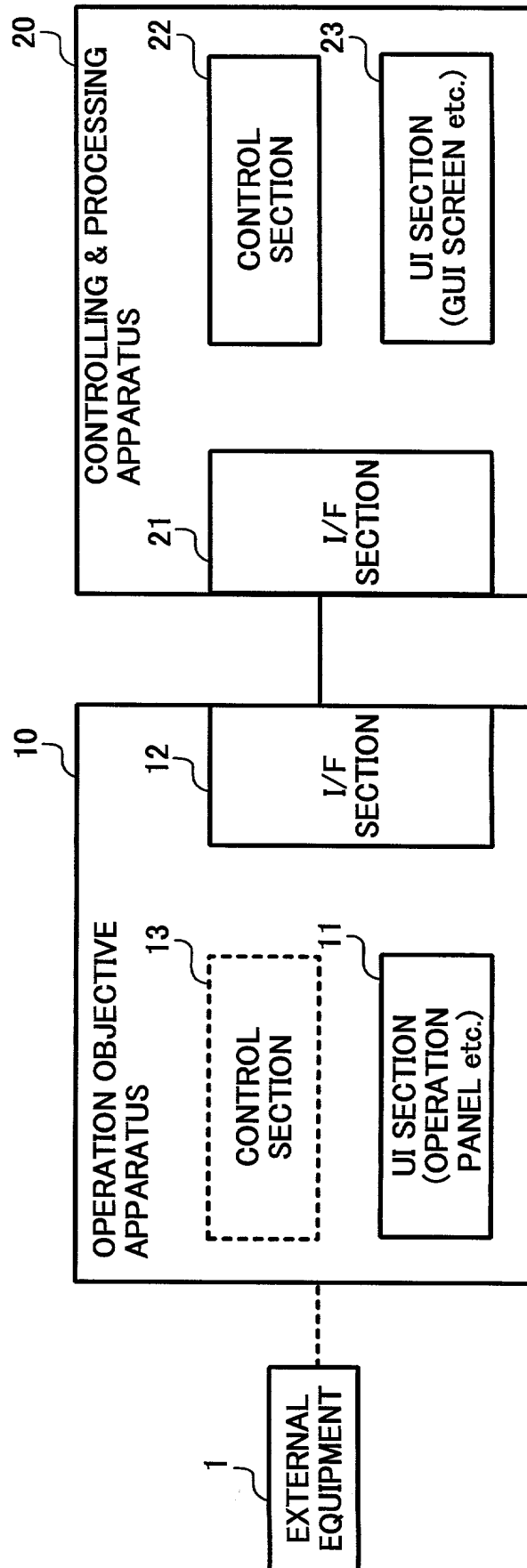


FIG. 55A

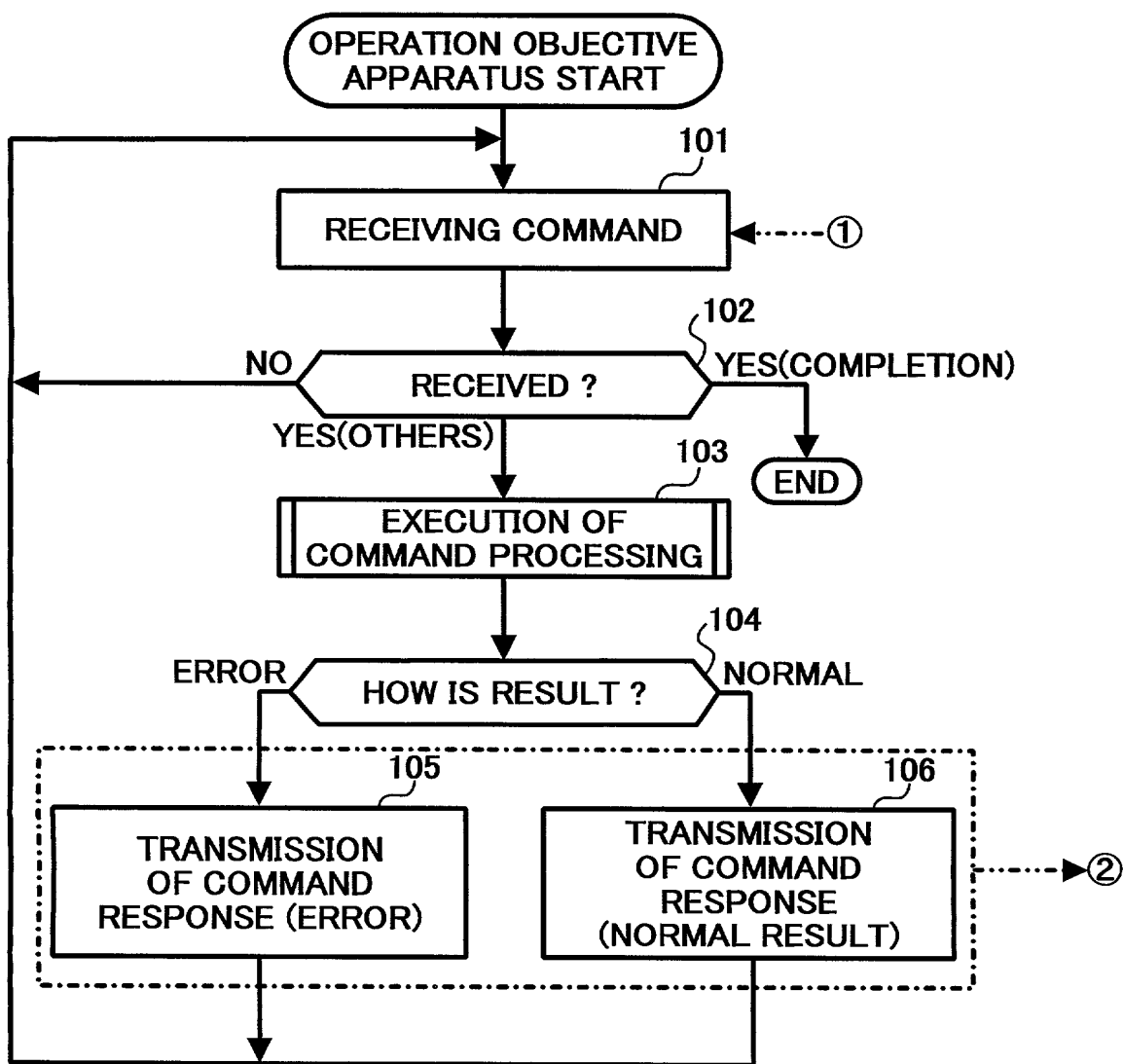


FIG. 55B

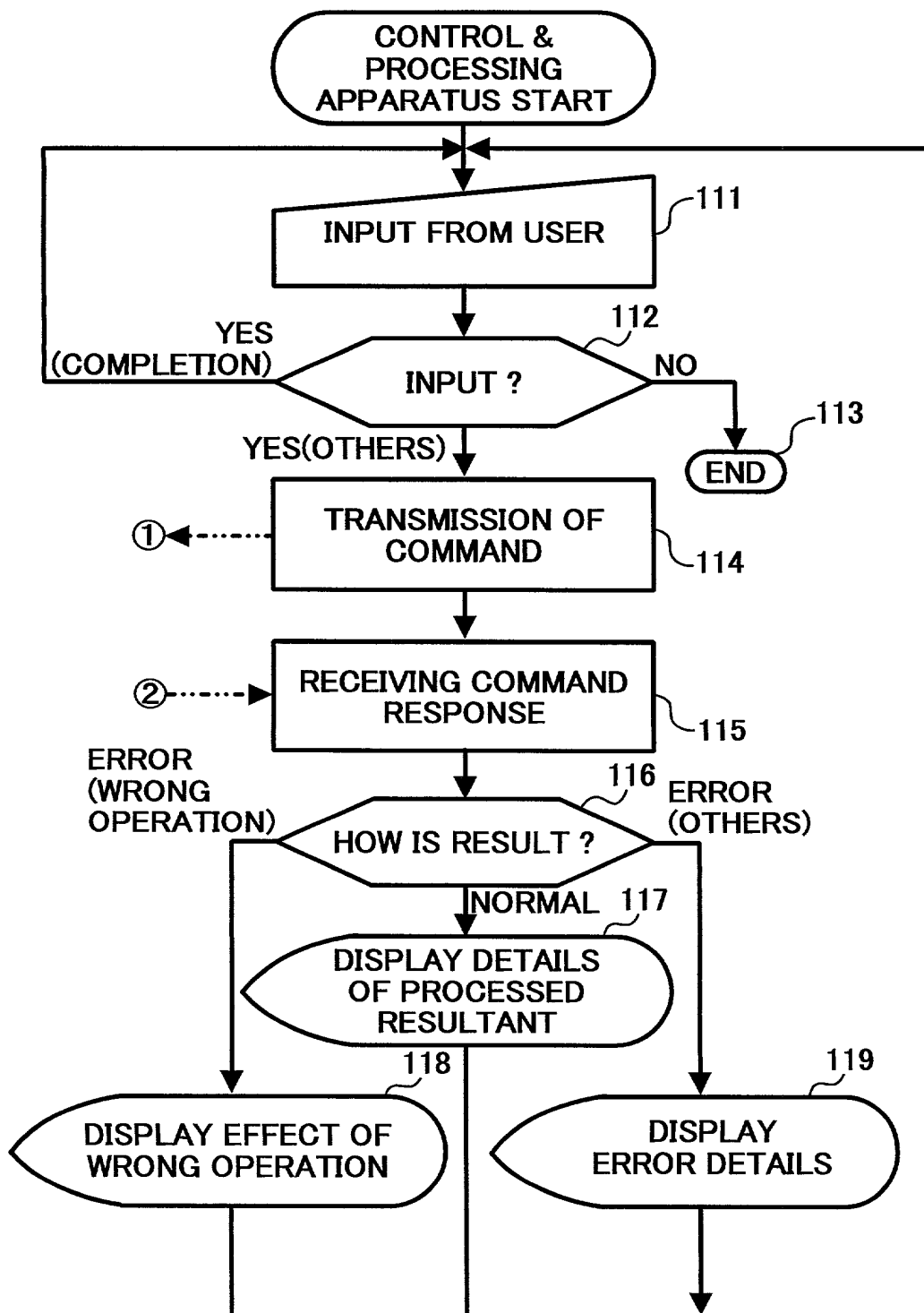


FIG. 56A

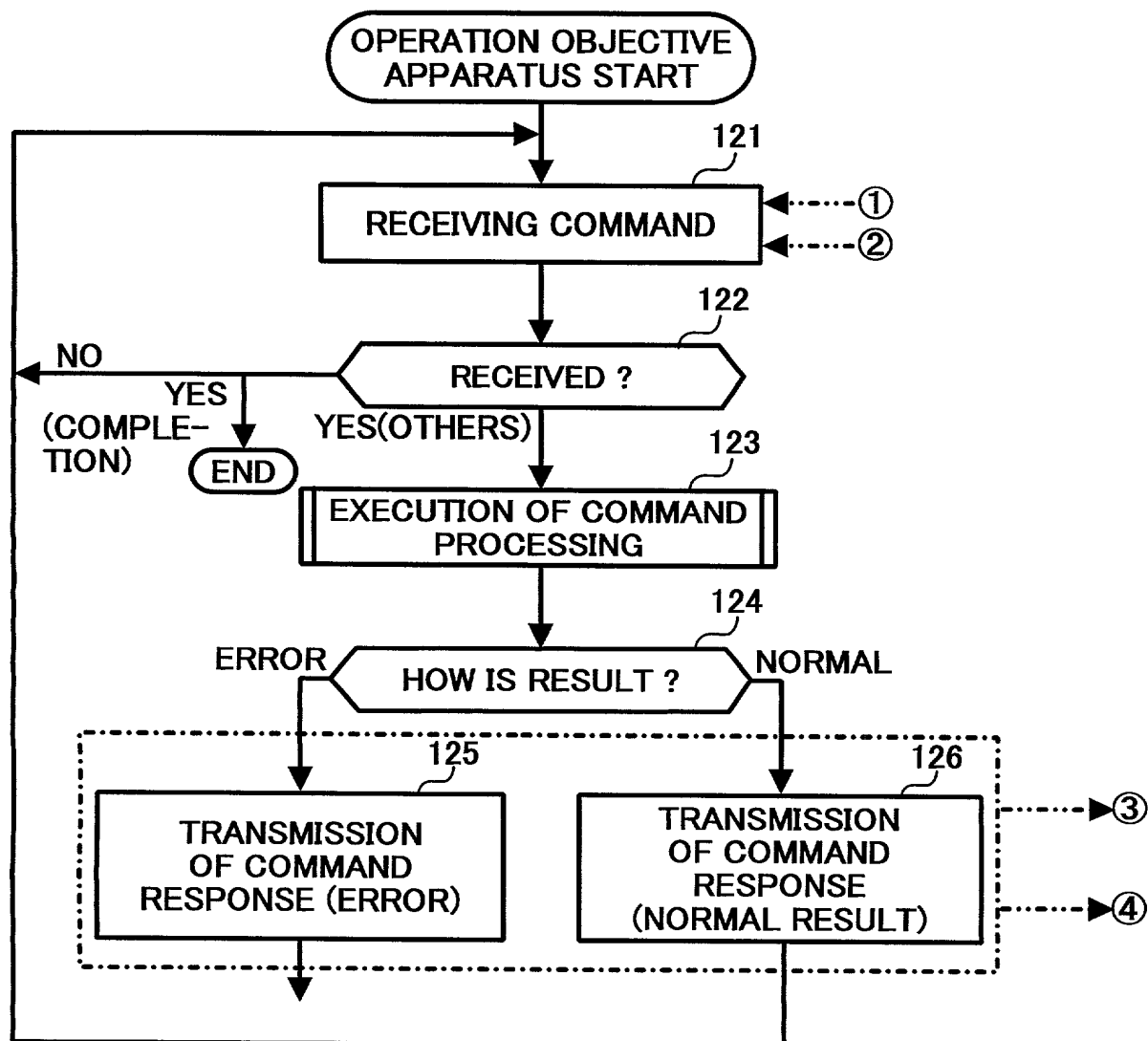


FIG. 56B

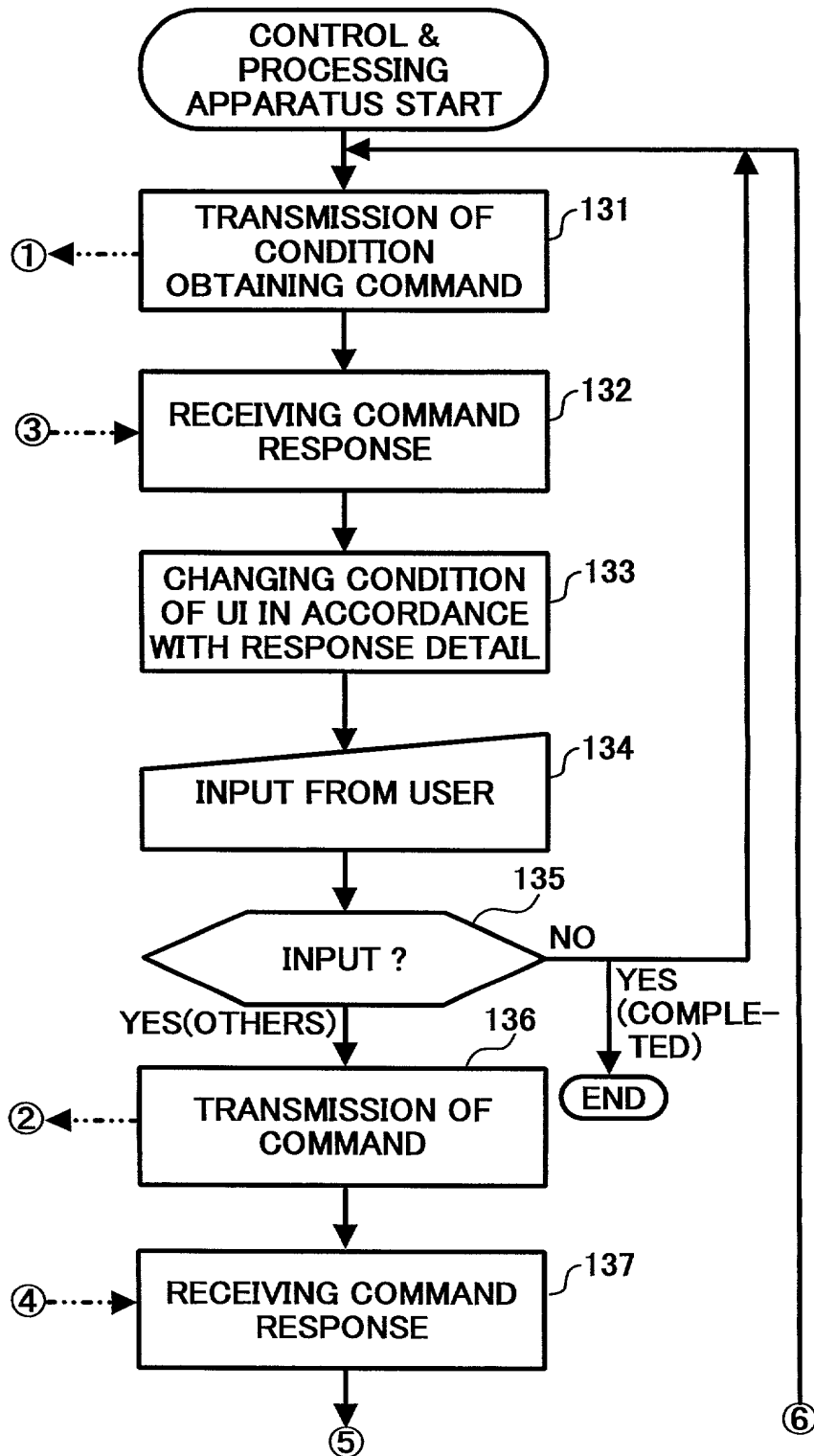


FIG. 56C

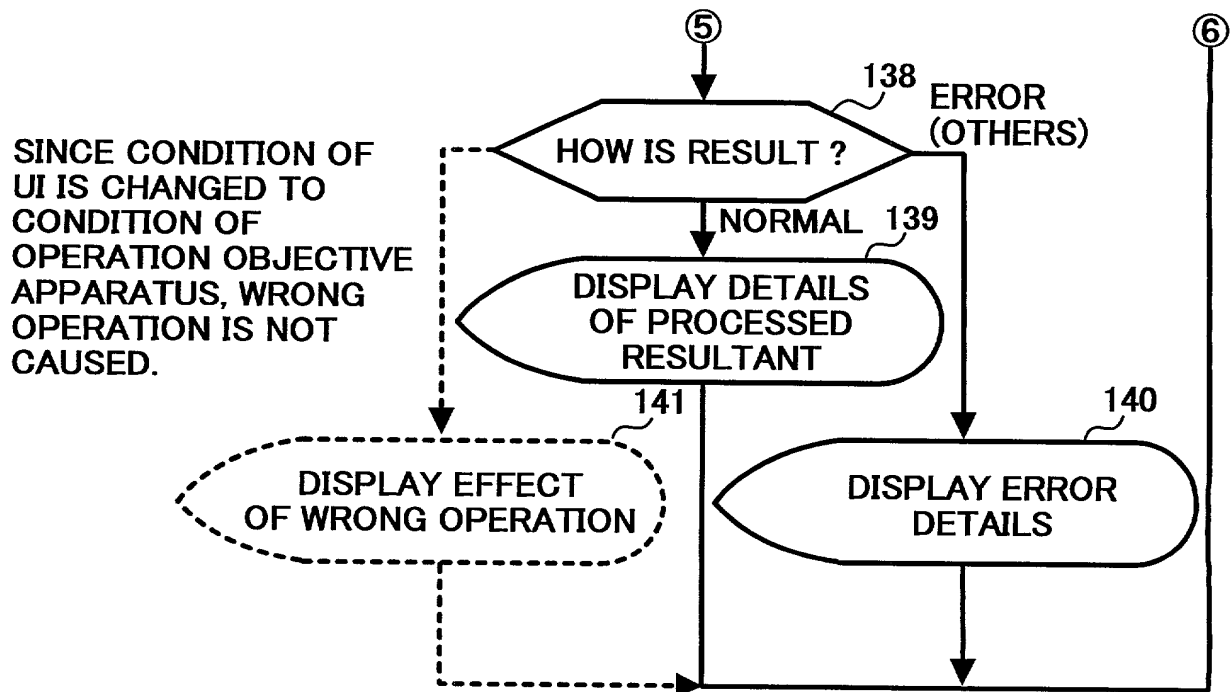


FIG. 57A

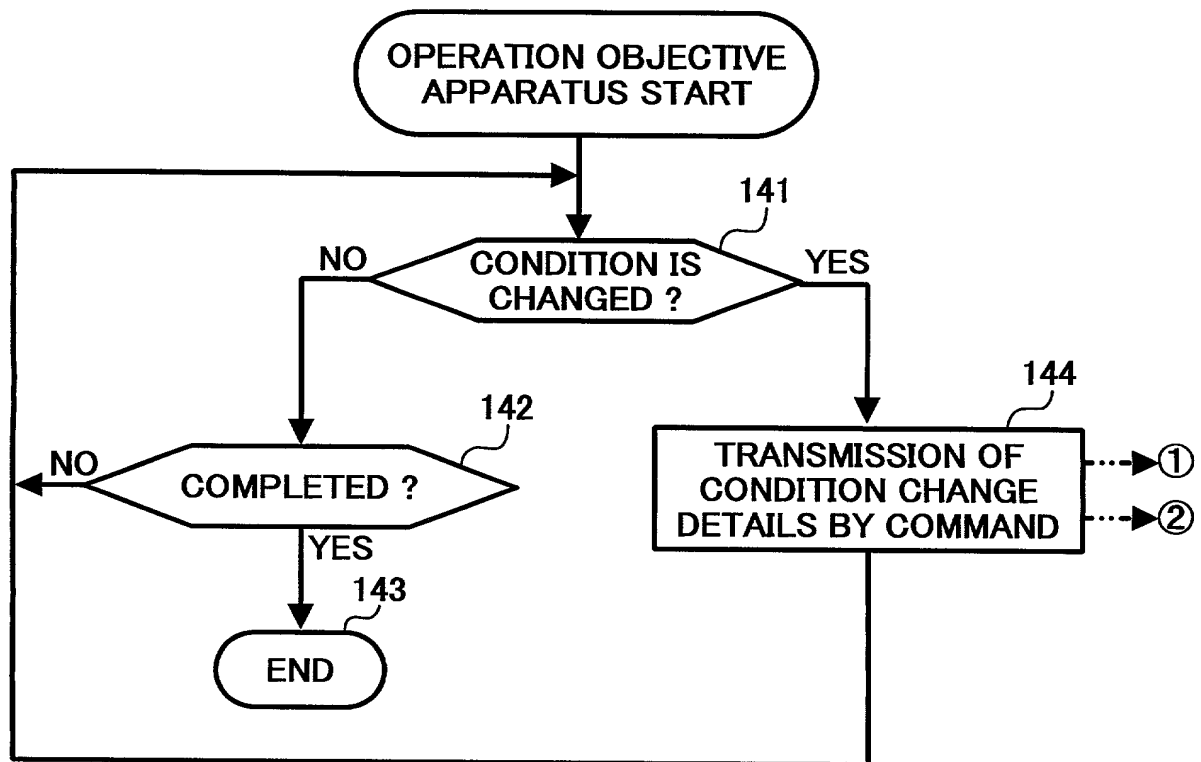


FIG. 57B

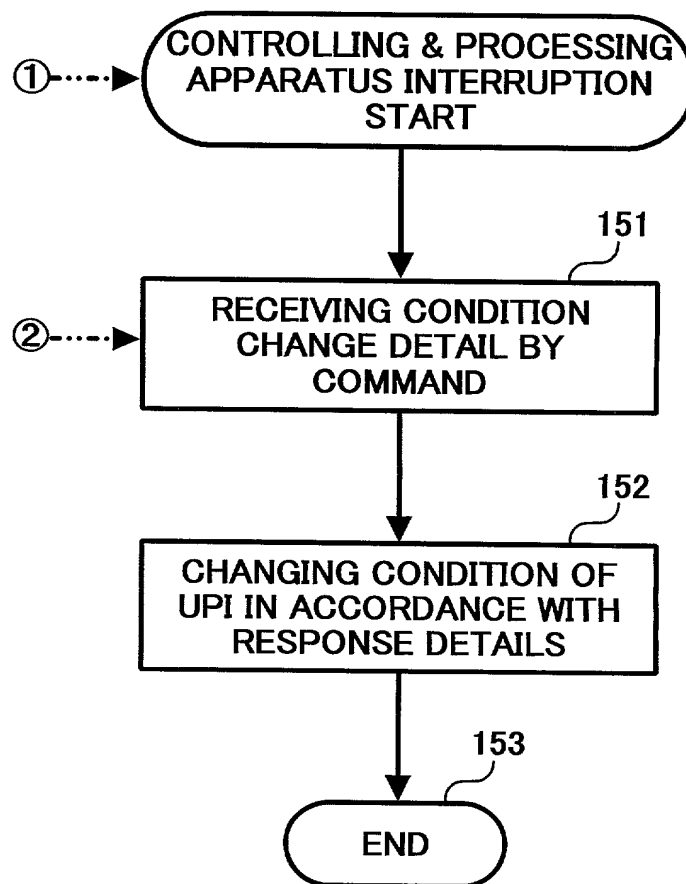


FIG. 57C

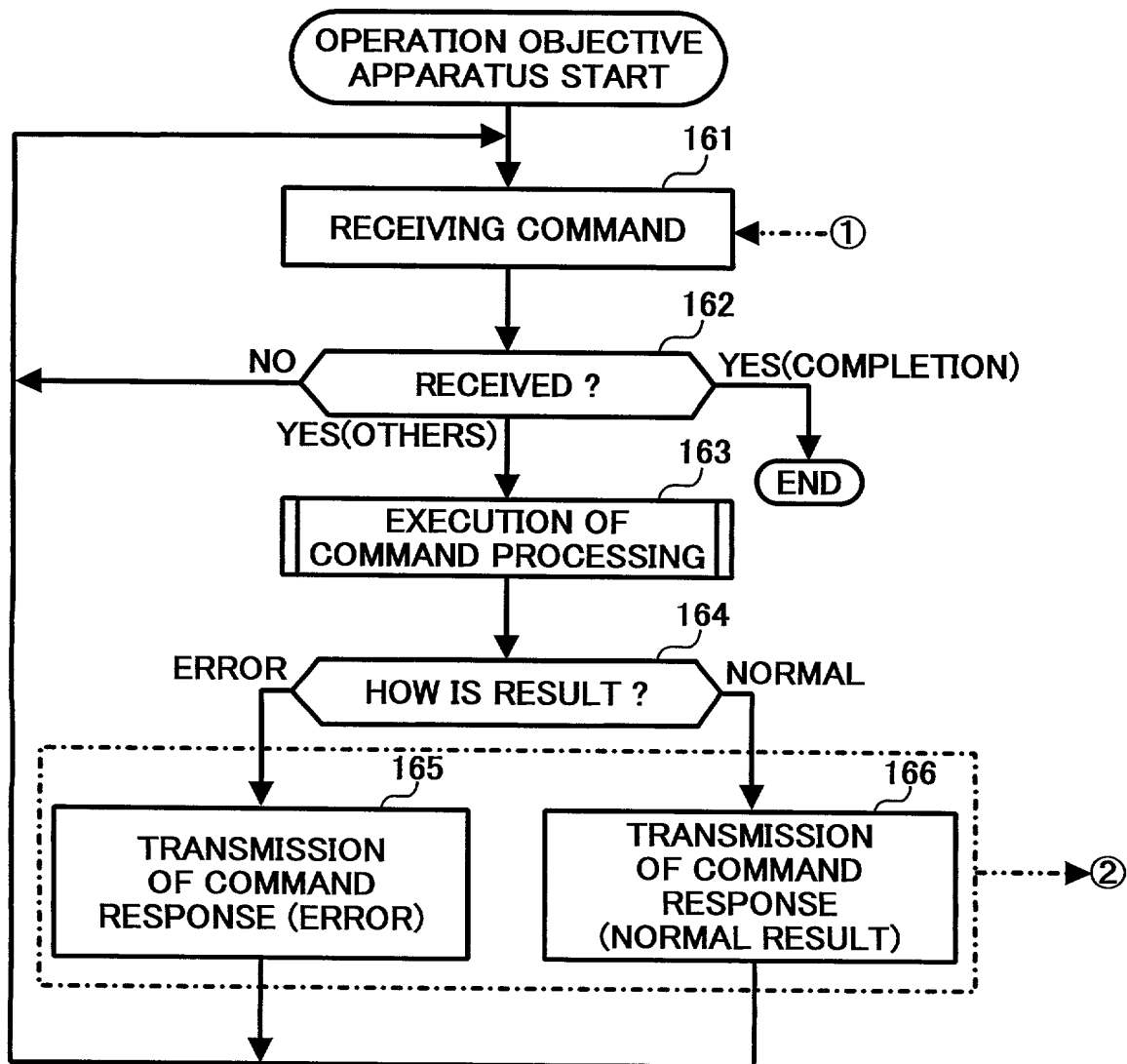


FIG. 57D

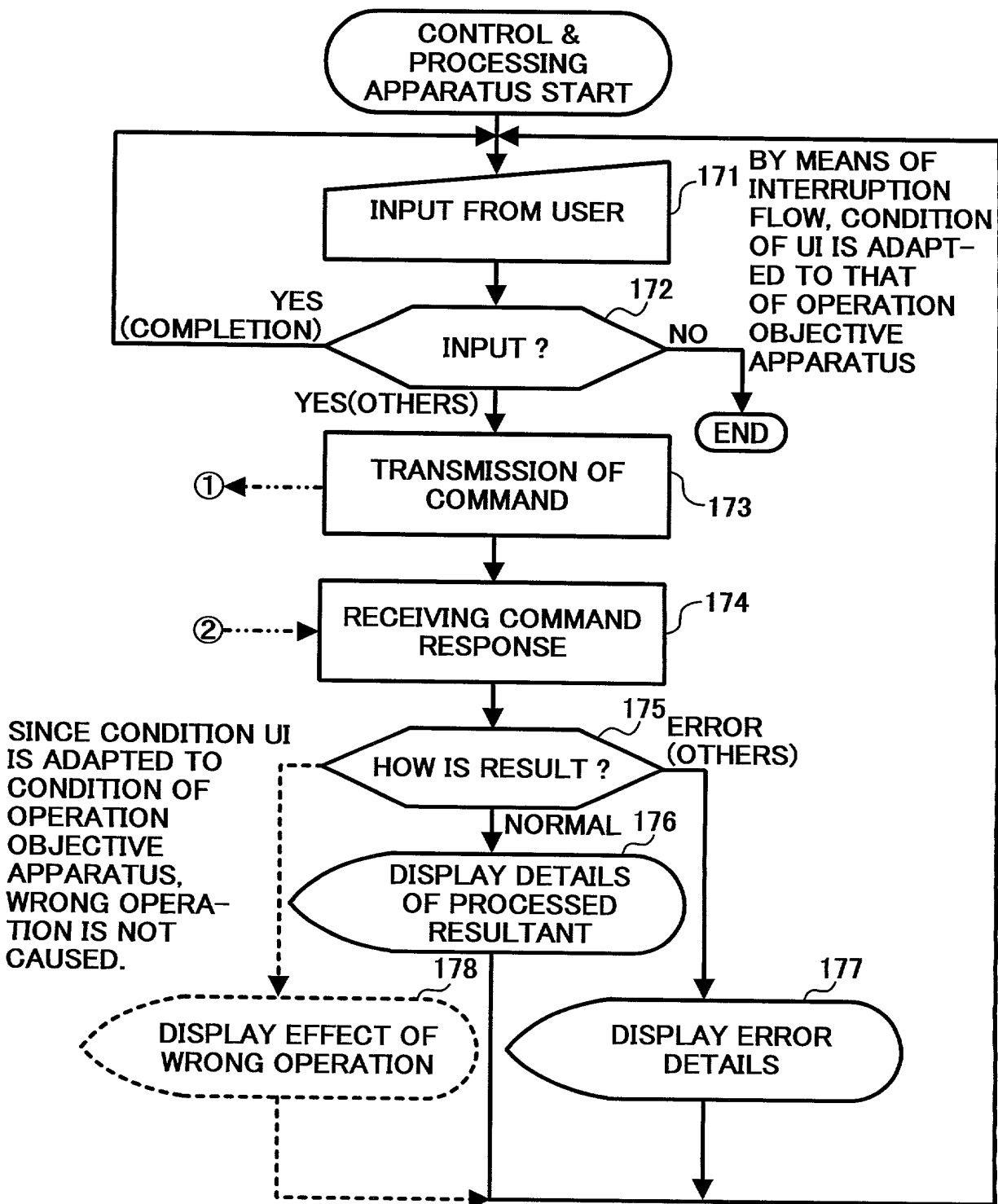


FIG. 58A

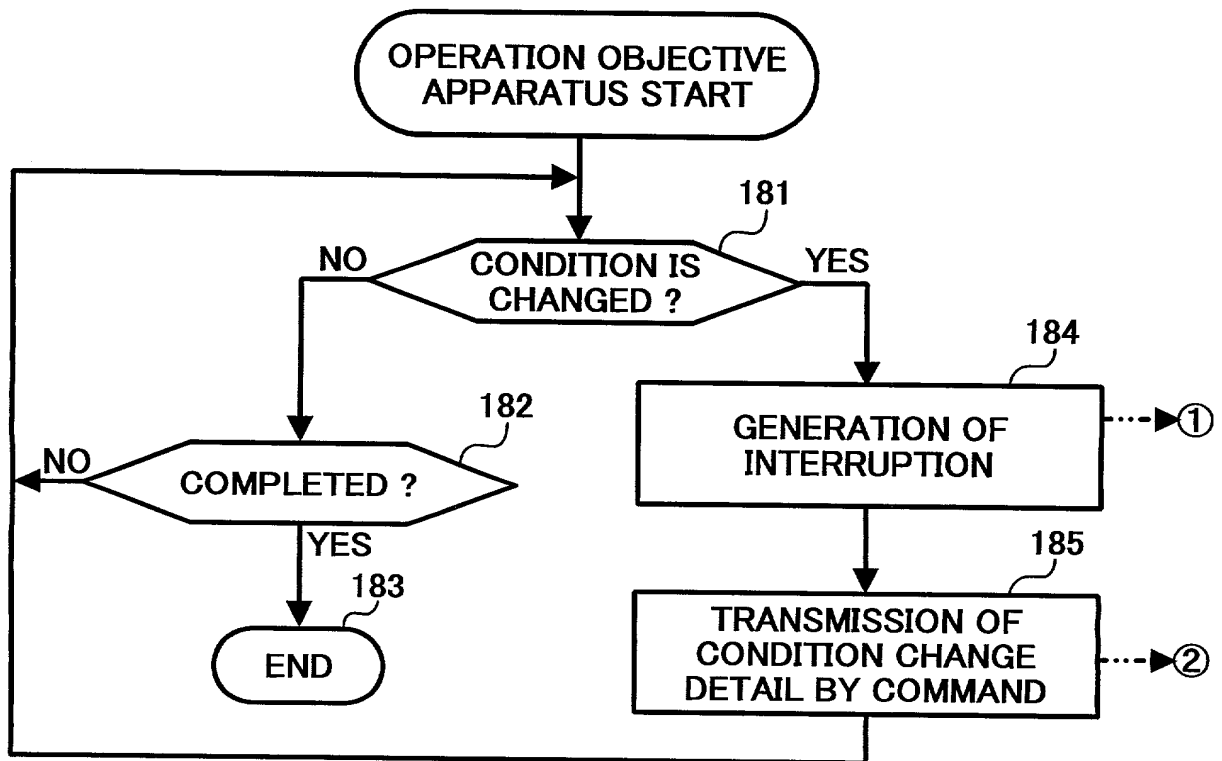


FIG. 58B

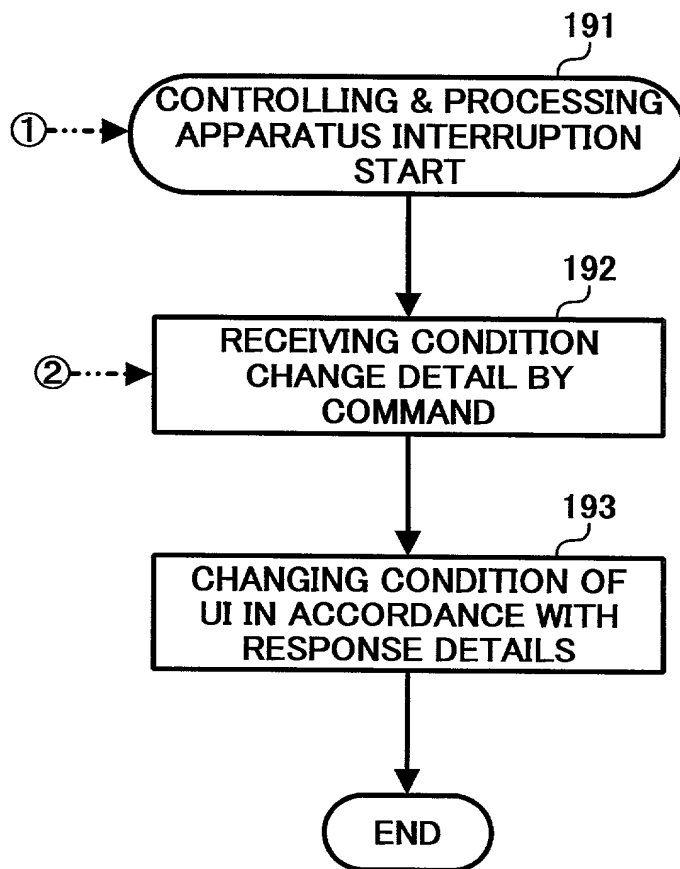


FIG. 58C

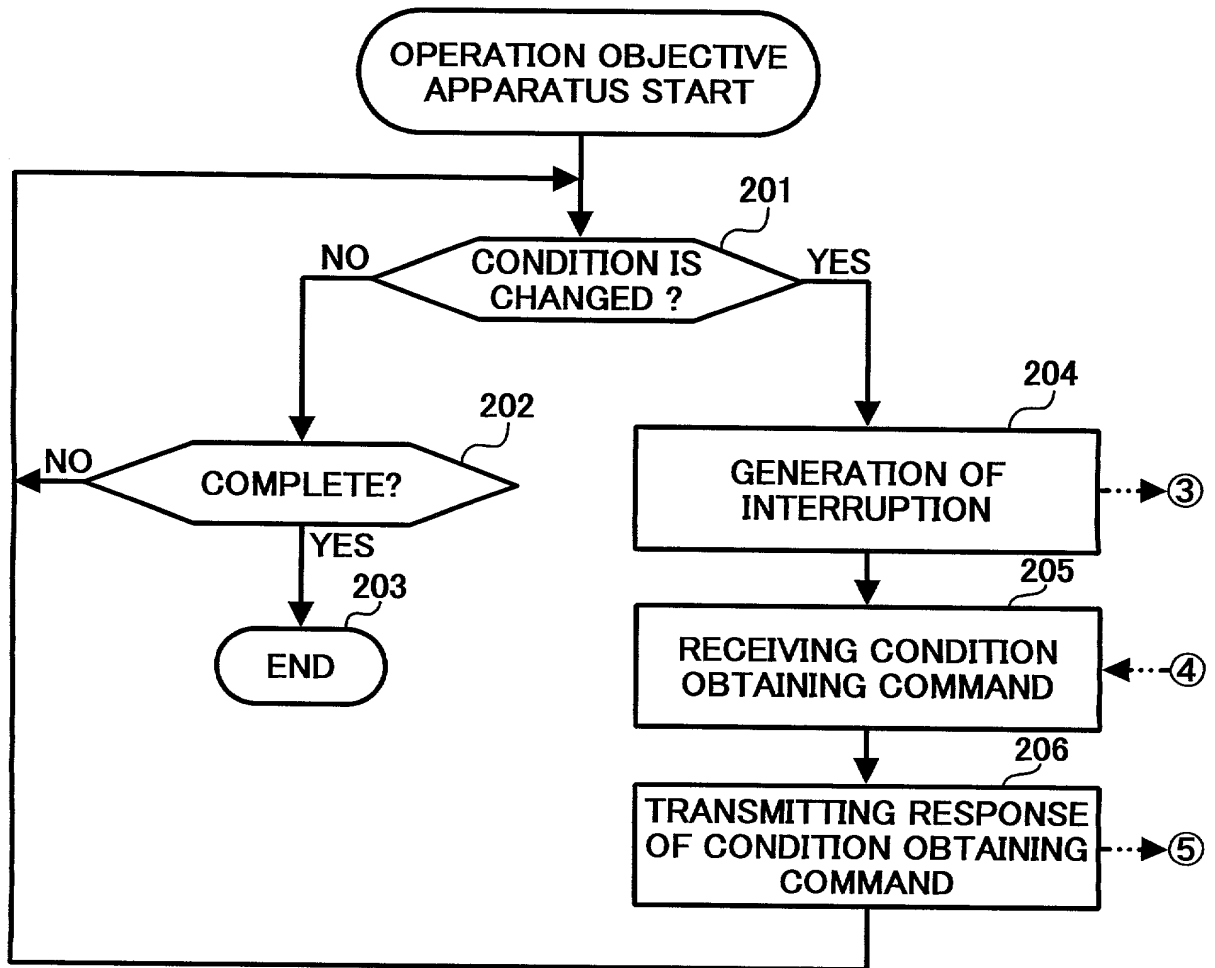


FIG. 58D

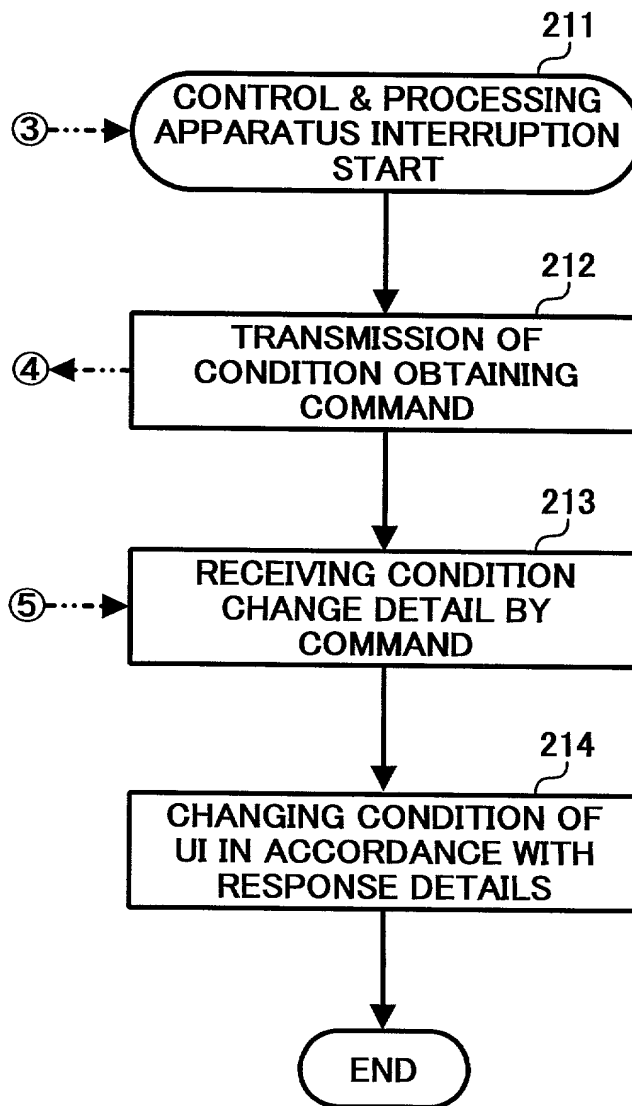


FIG. 58D

FIG. 59A

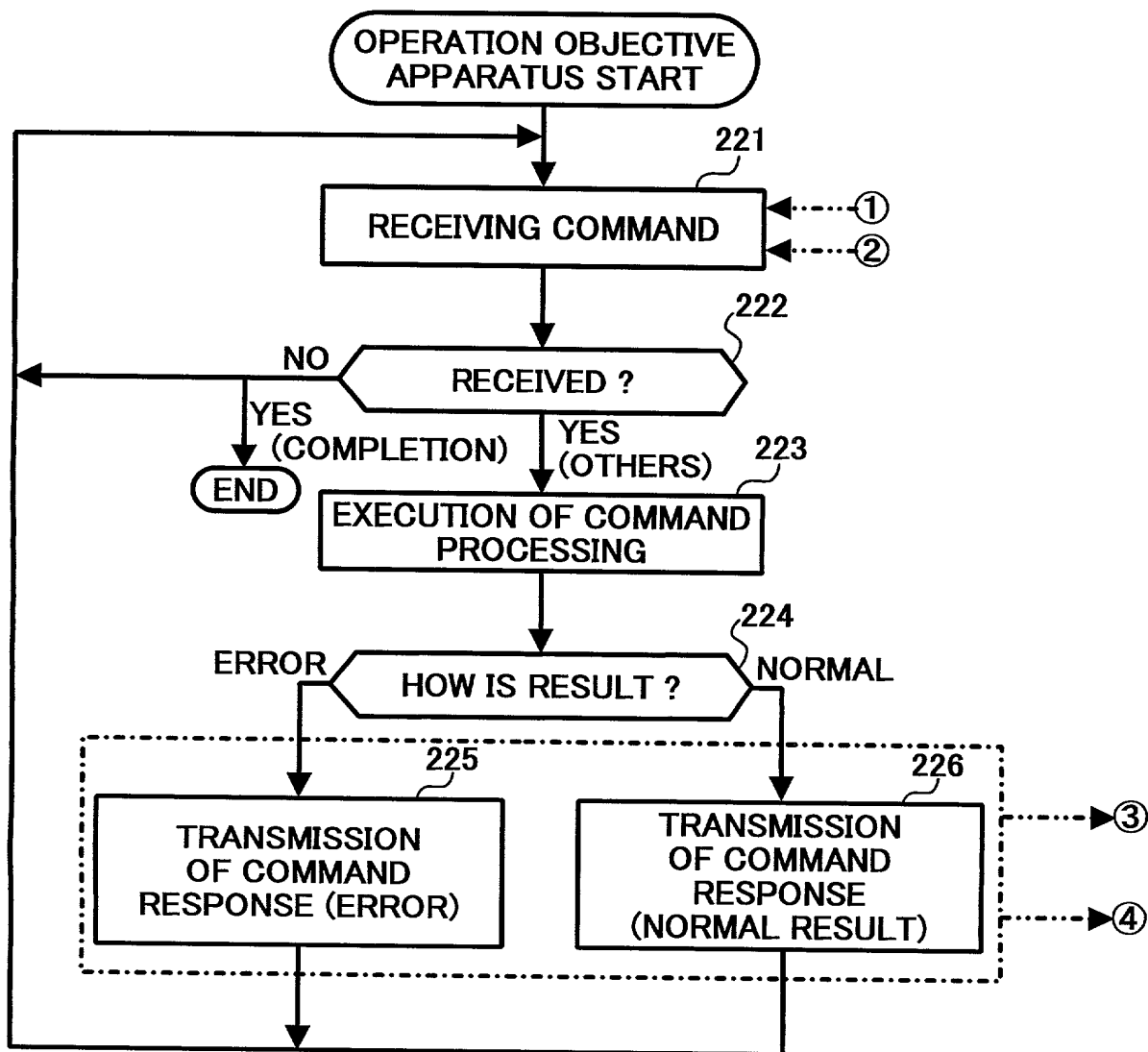


FIG. 59B

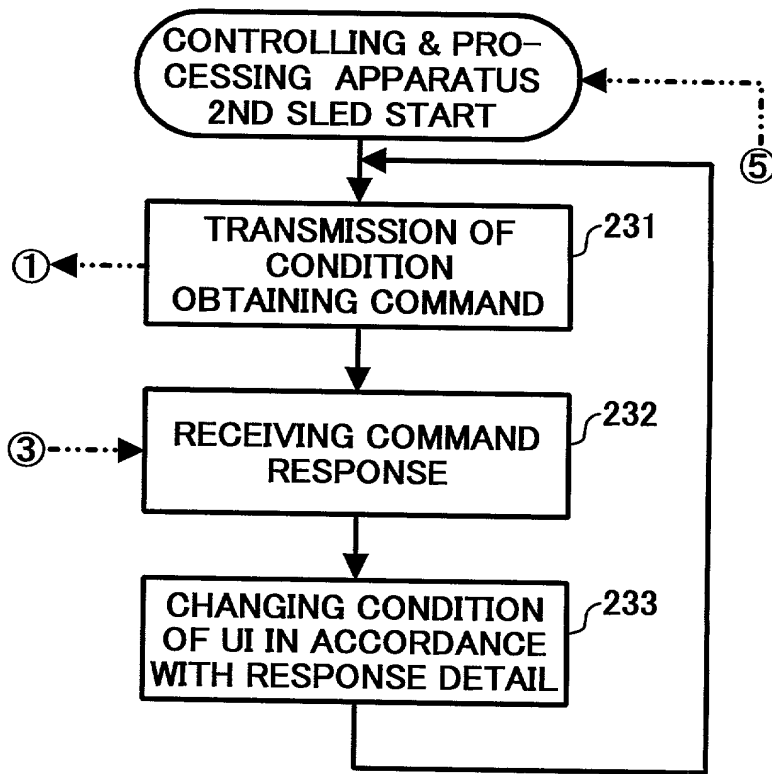


FIG. 59C

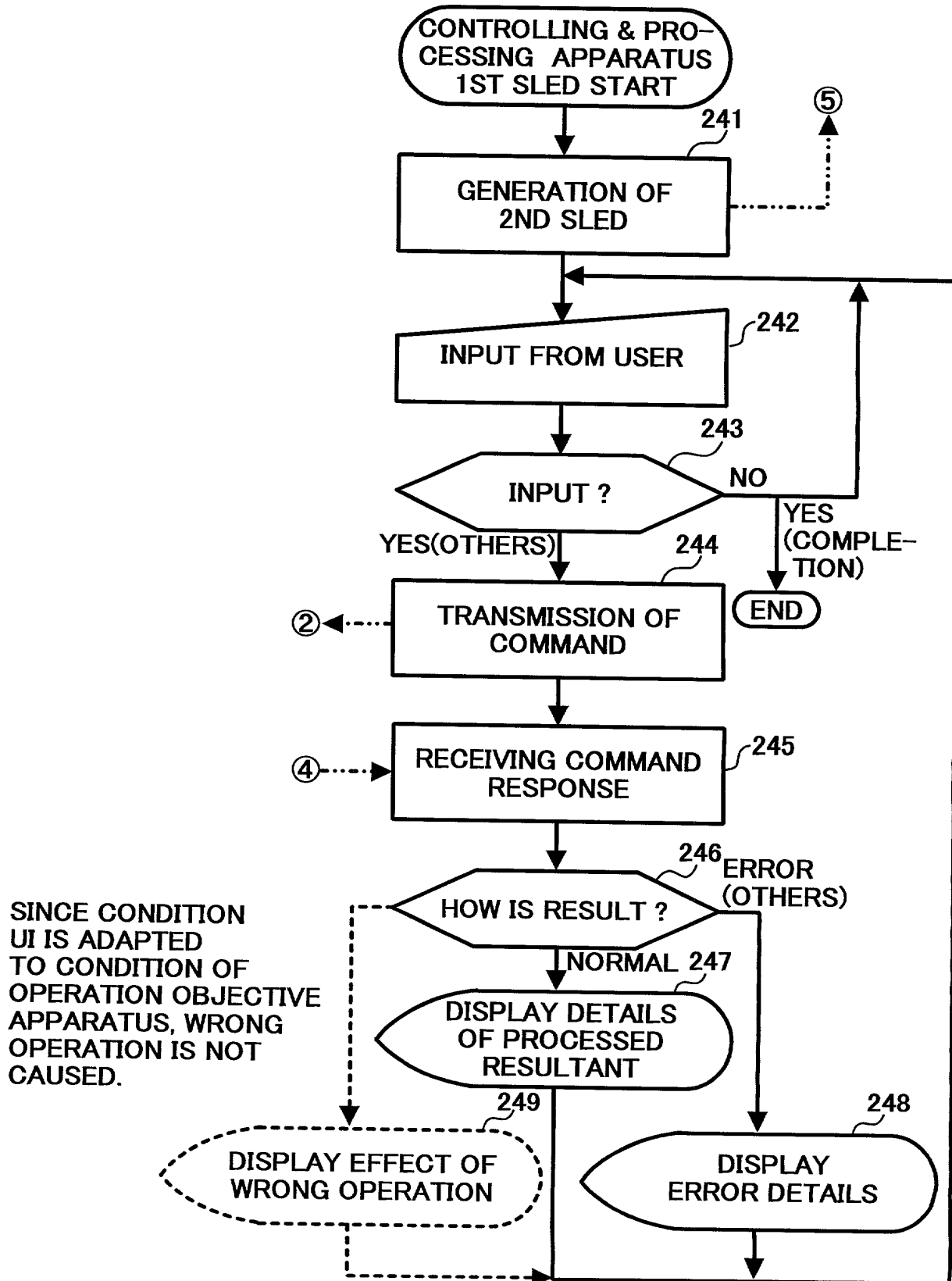


FIG. 60A

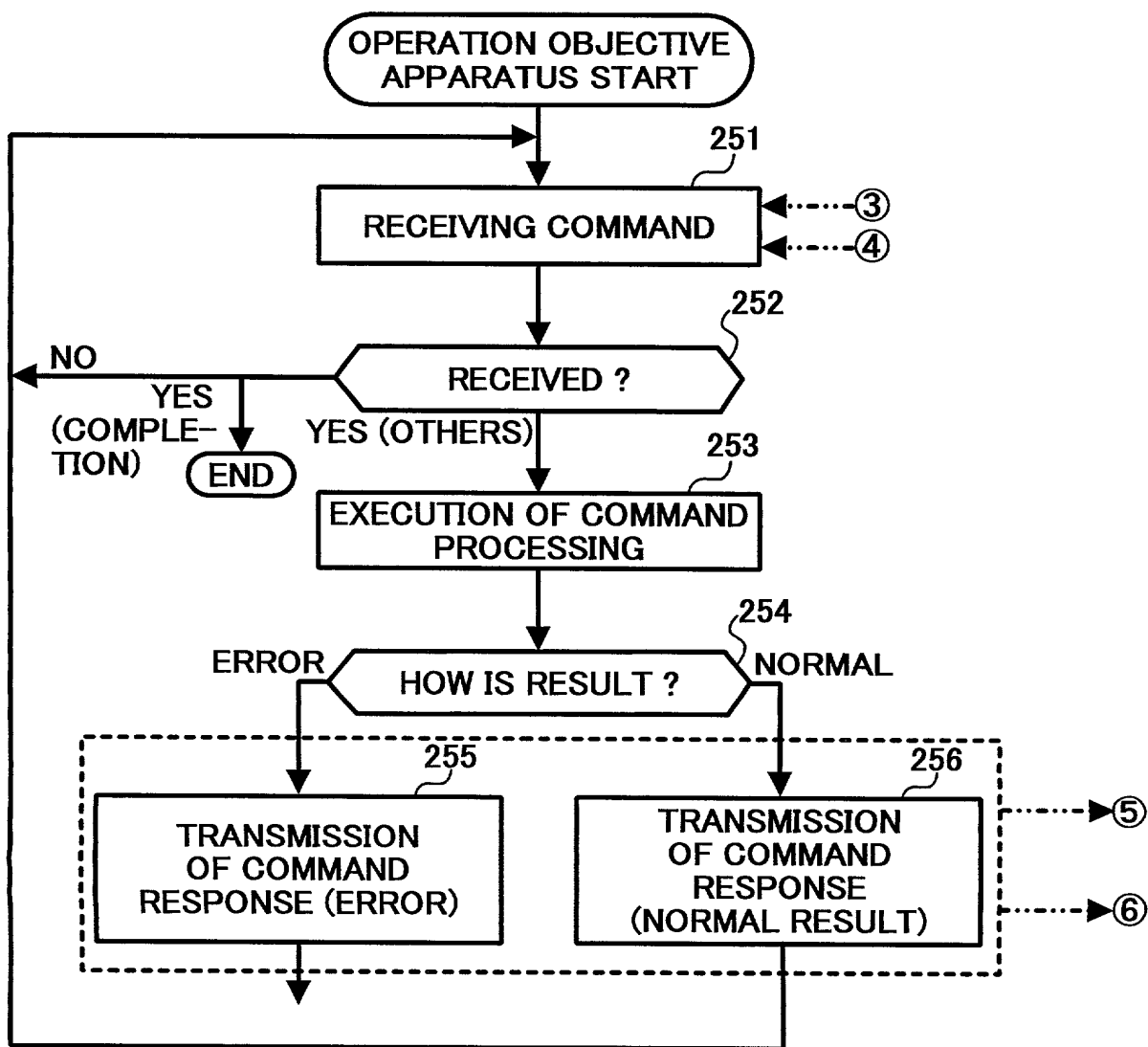


FIG. 60B

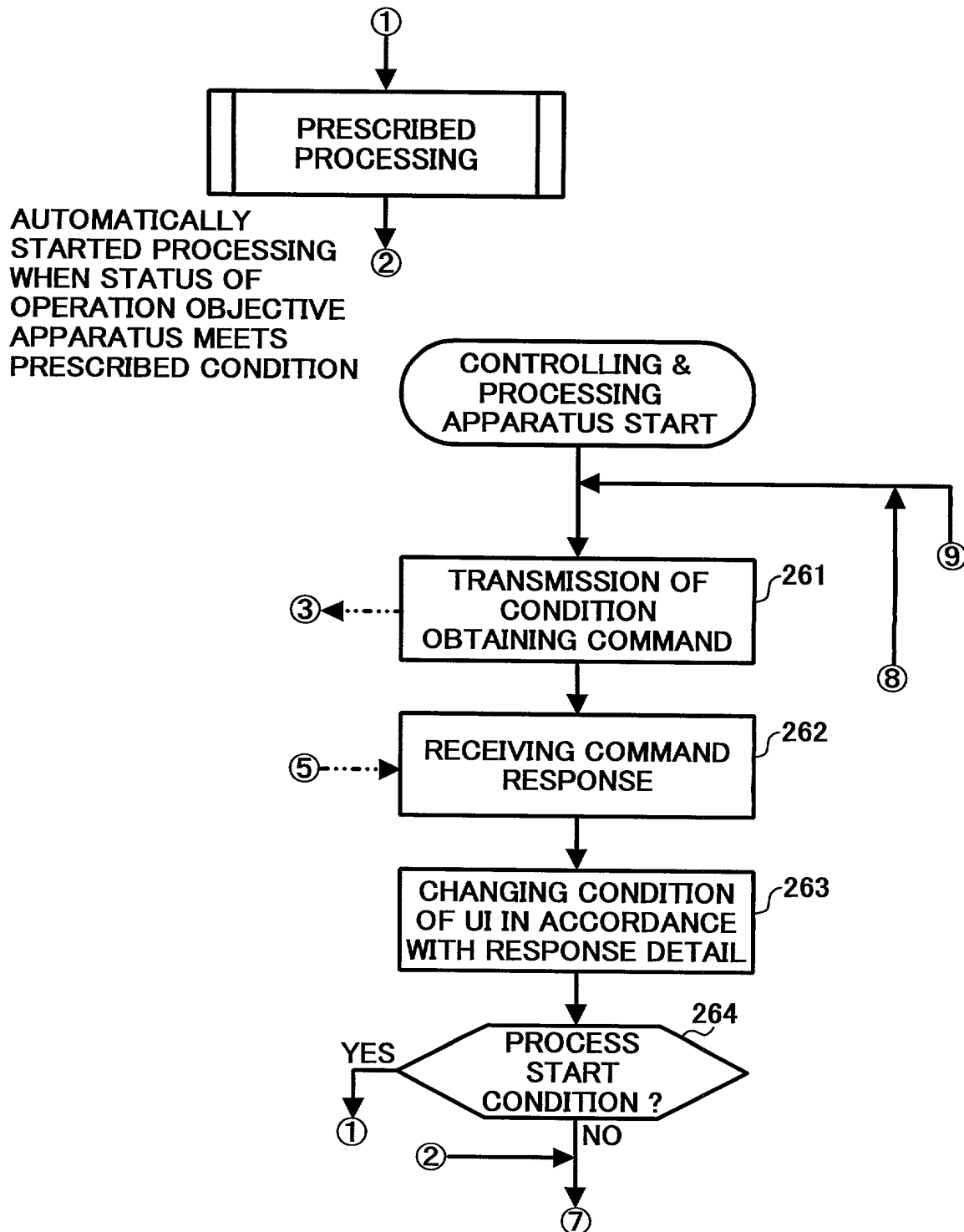


FIG. 60C

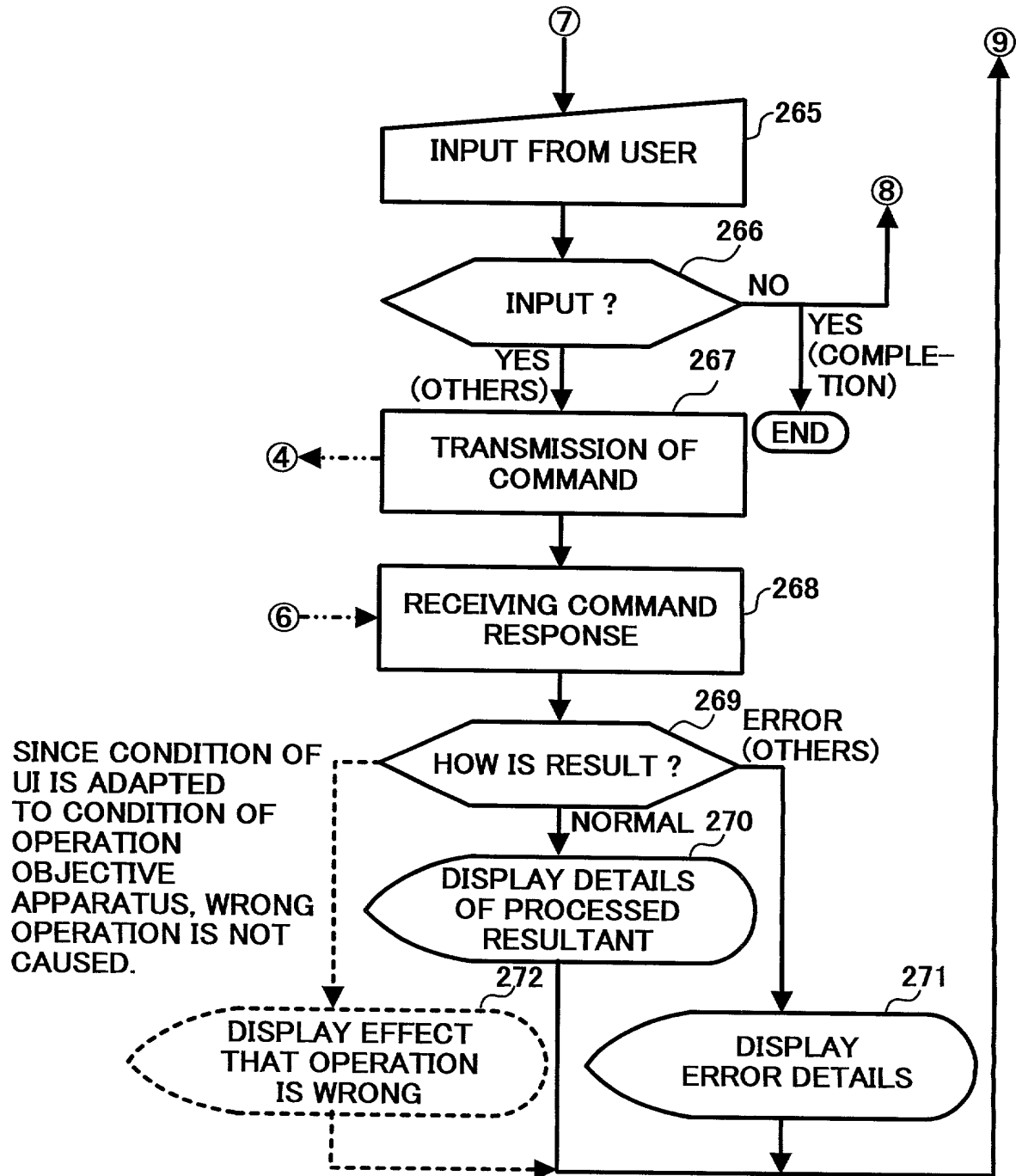


FIG. 61A

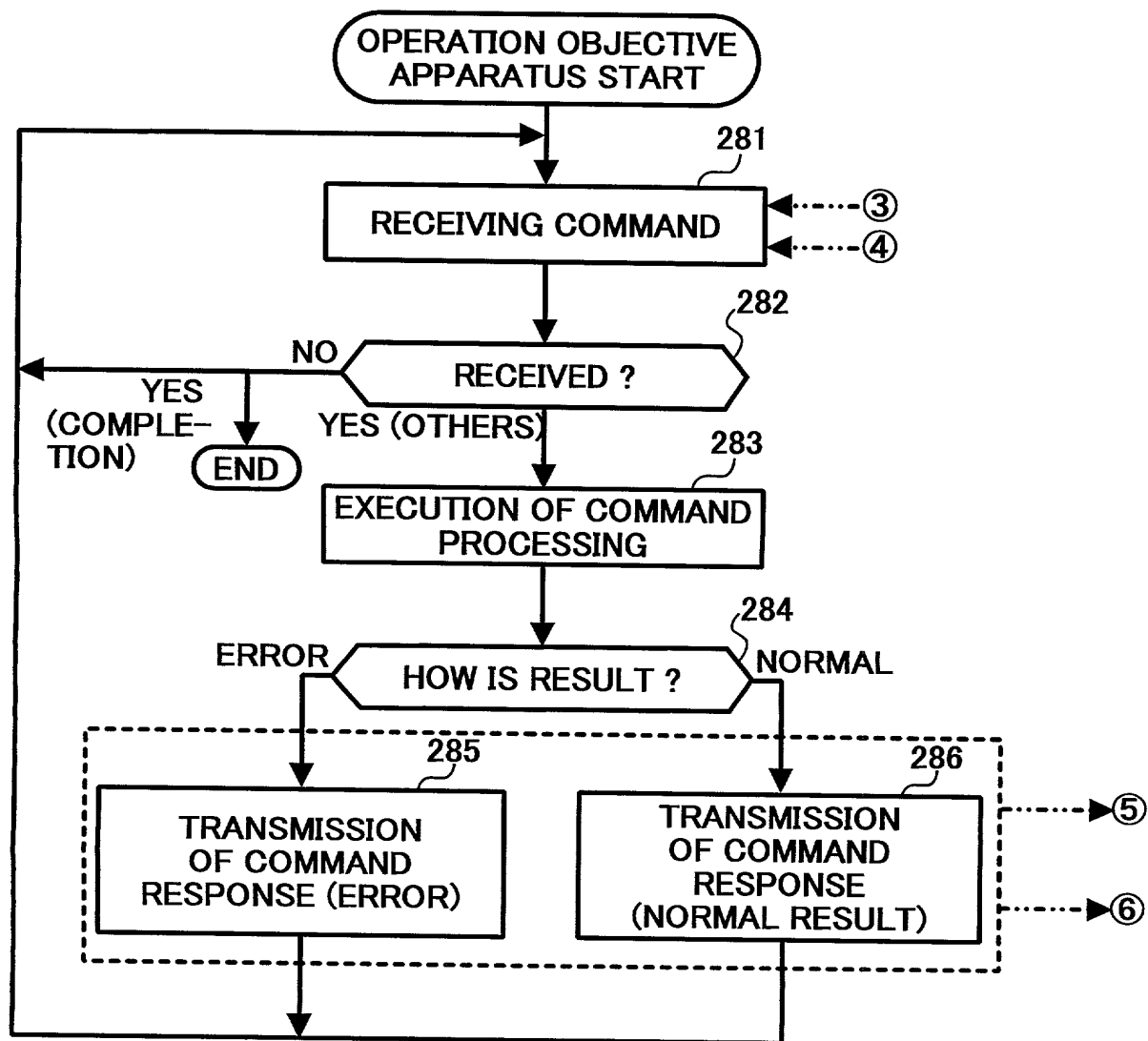


FIG. 61B

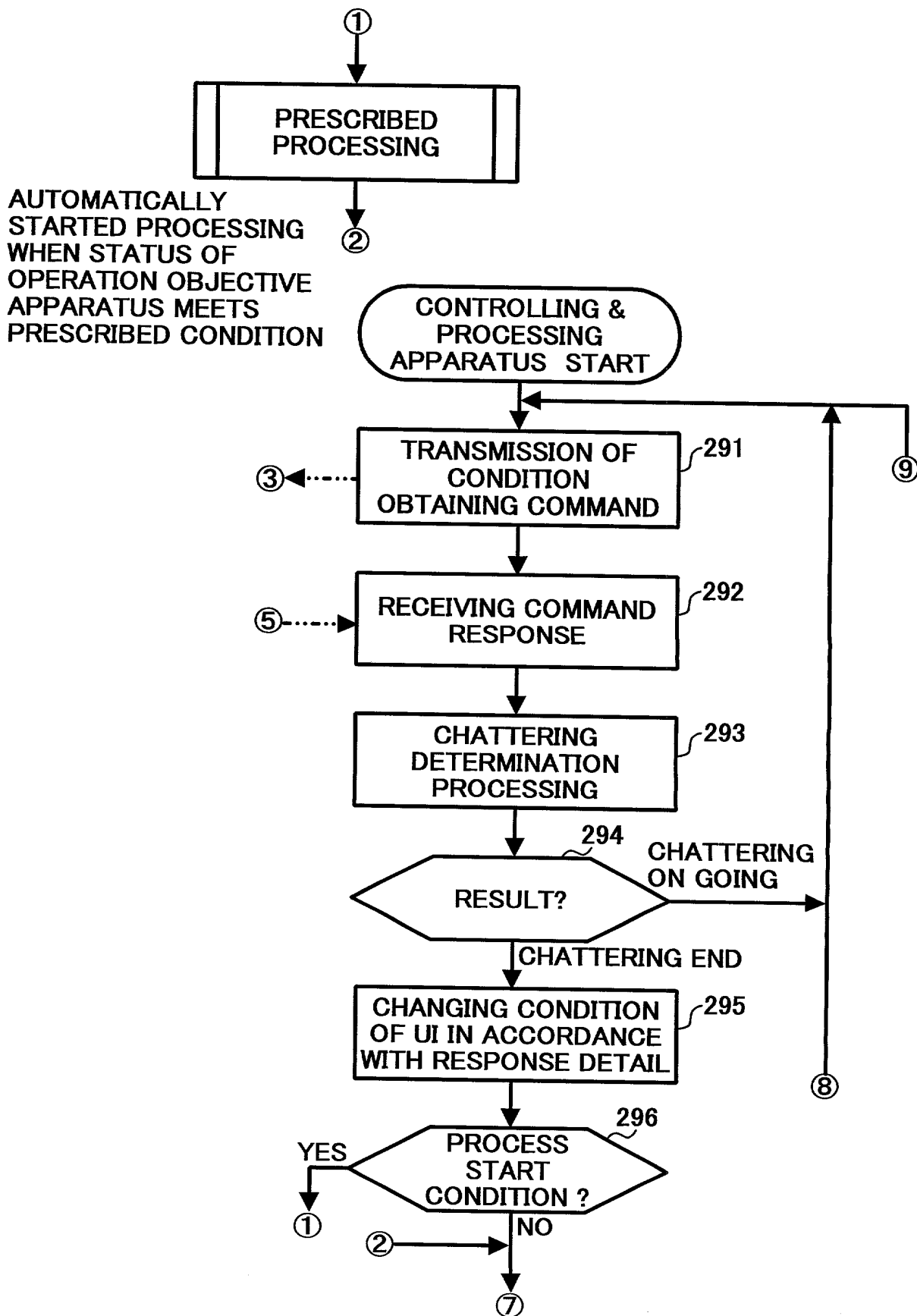


FIG. 61C

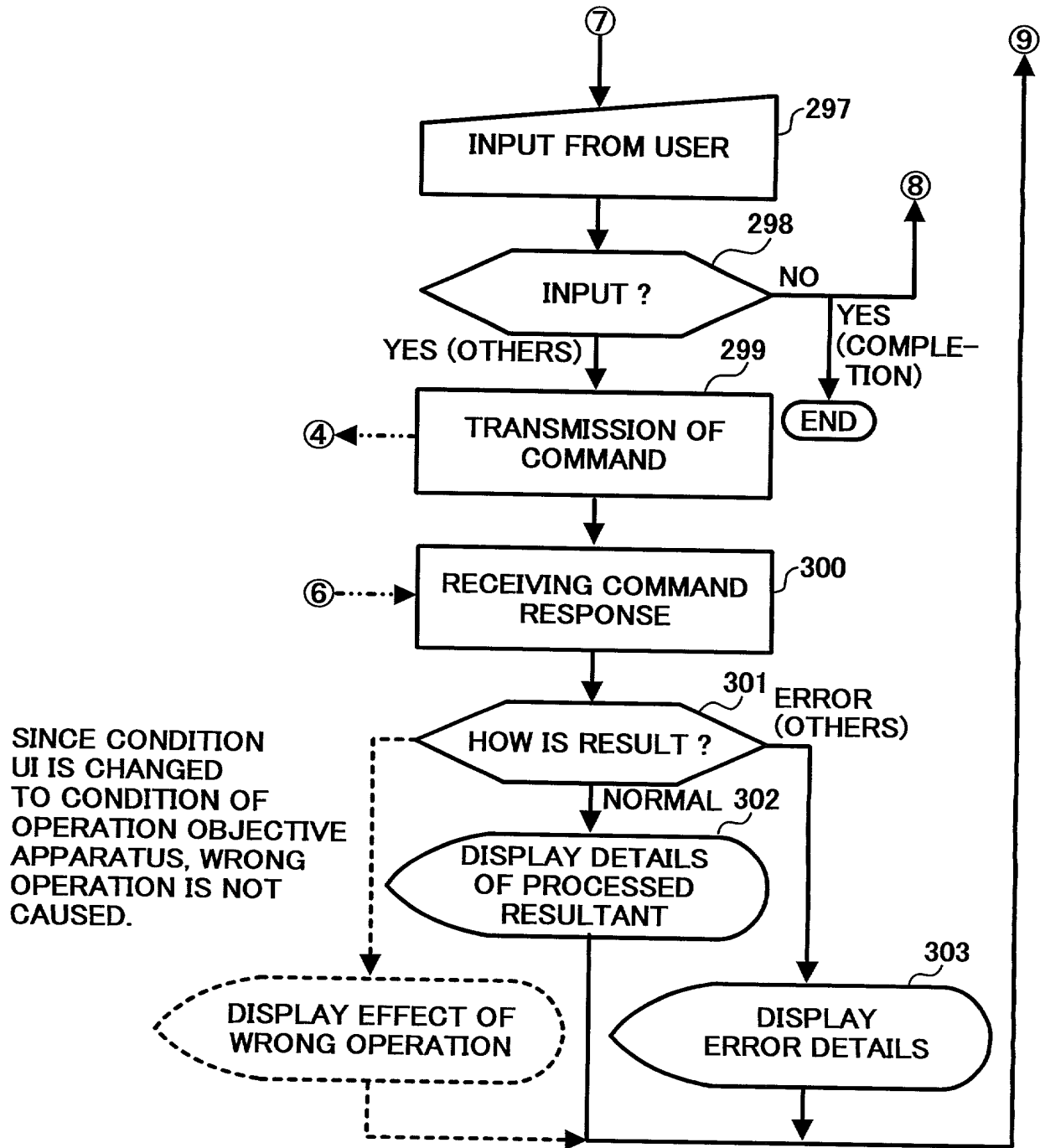


FIG. 62A

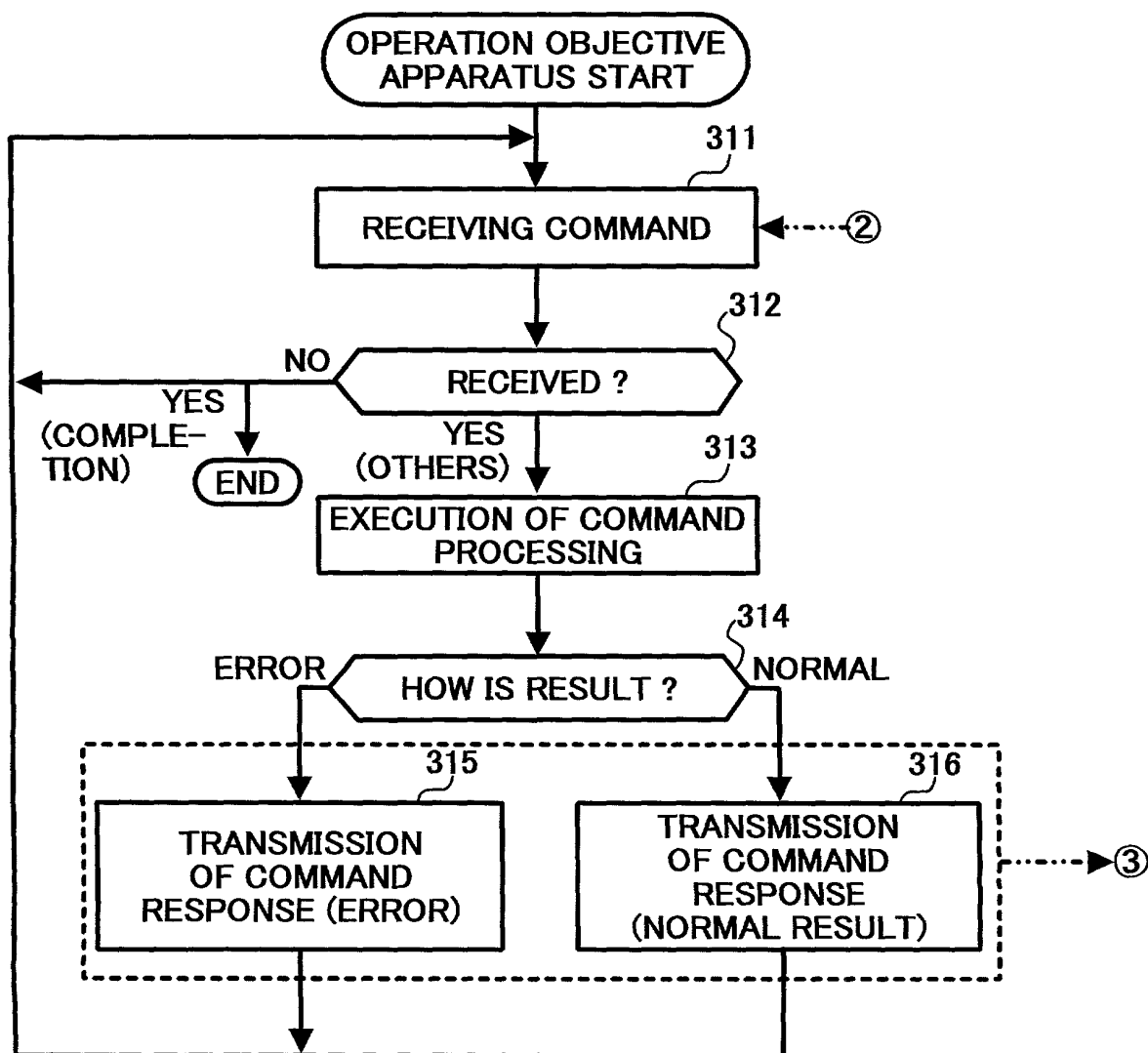


FIG. 62B

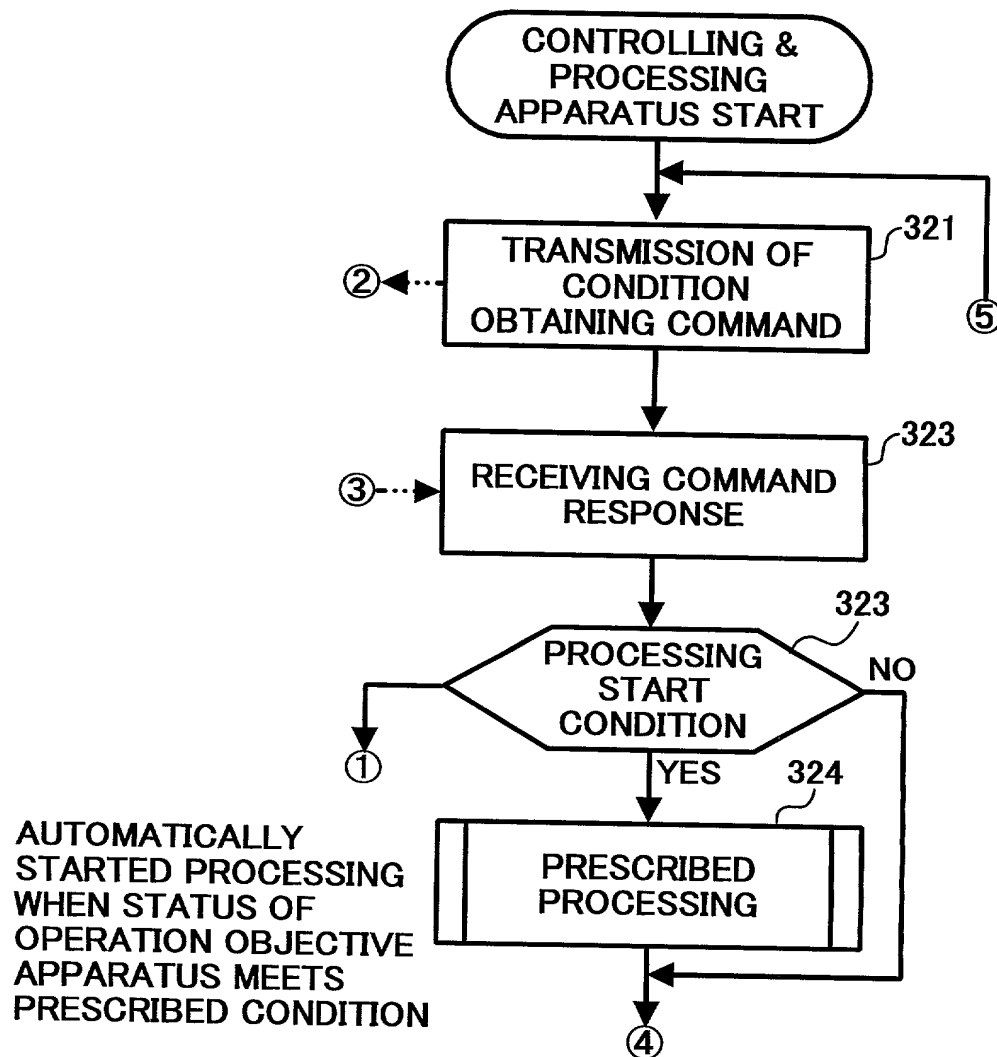


FIG. 62C

